

GA102 DT SKU P-BOARD

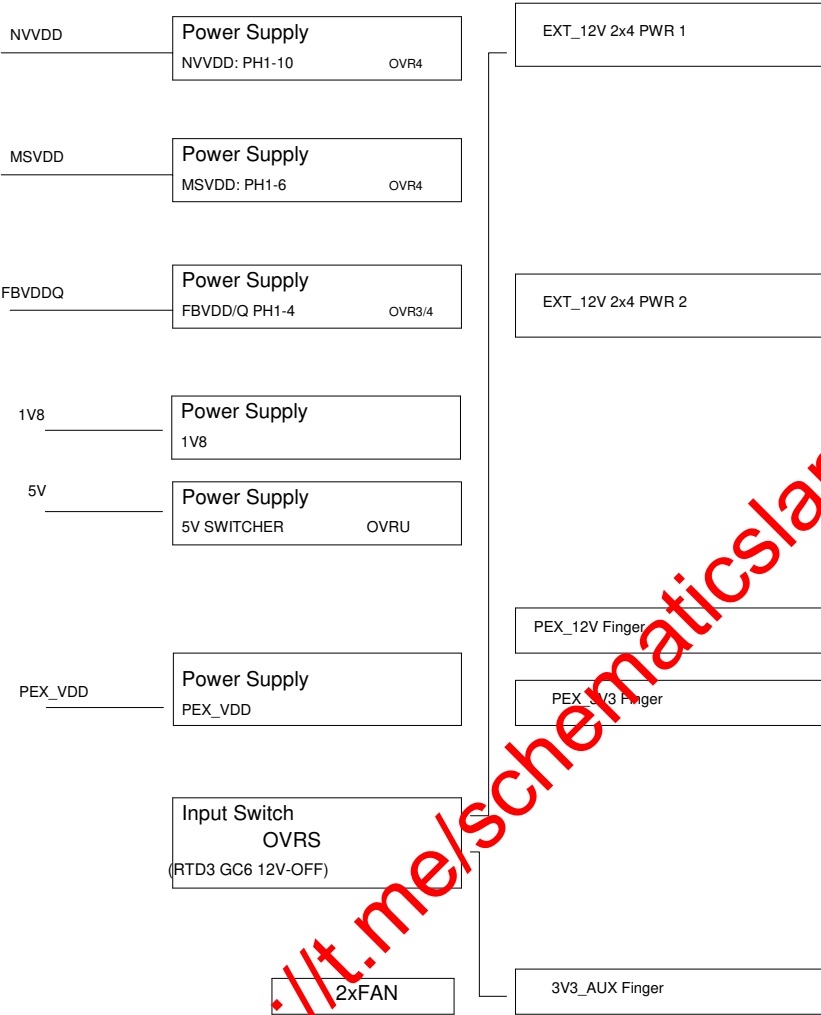
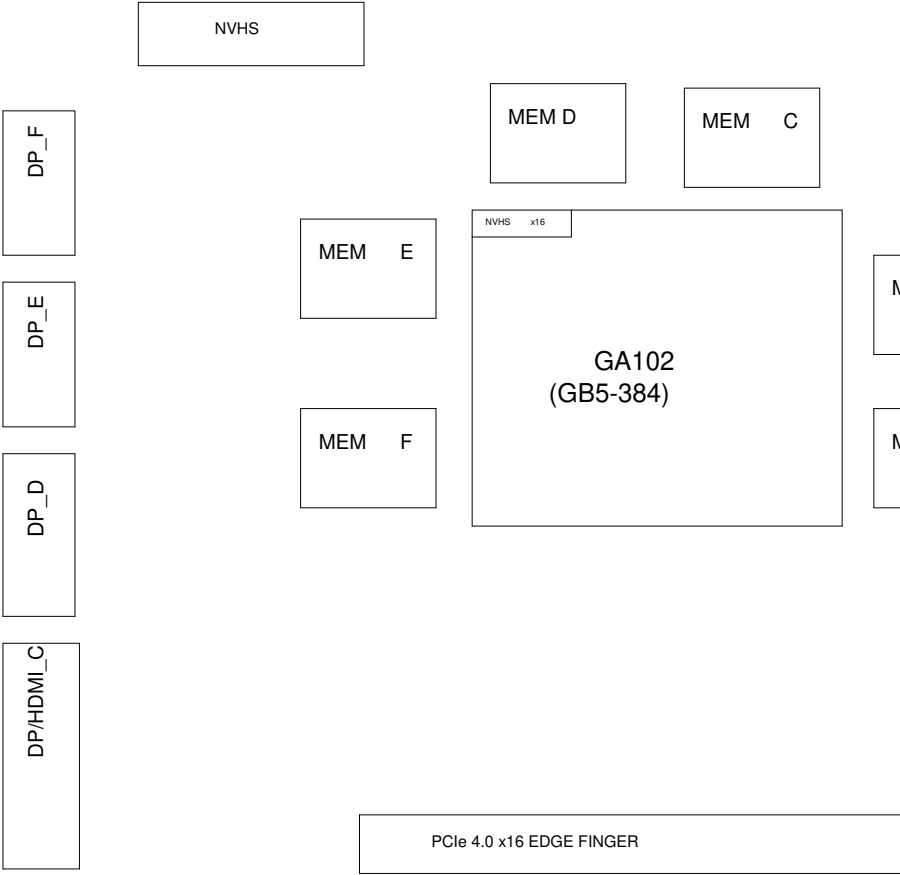
350W, FH Std PCB, 384b, GDDR6X x8
DP + DP + DP + HDMI/DP

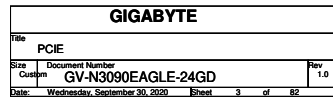
TABLE OF CONTENTS

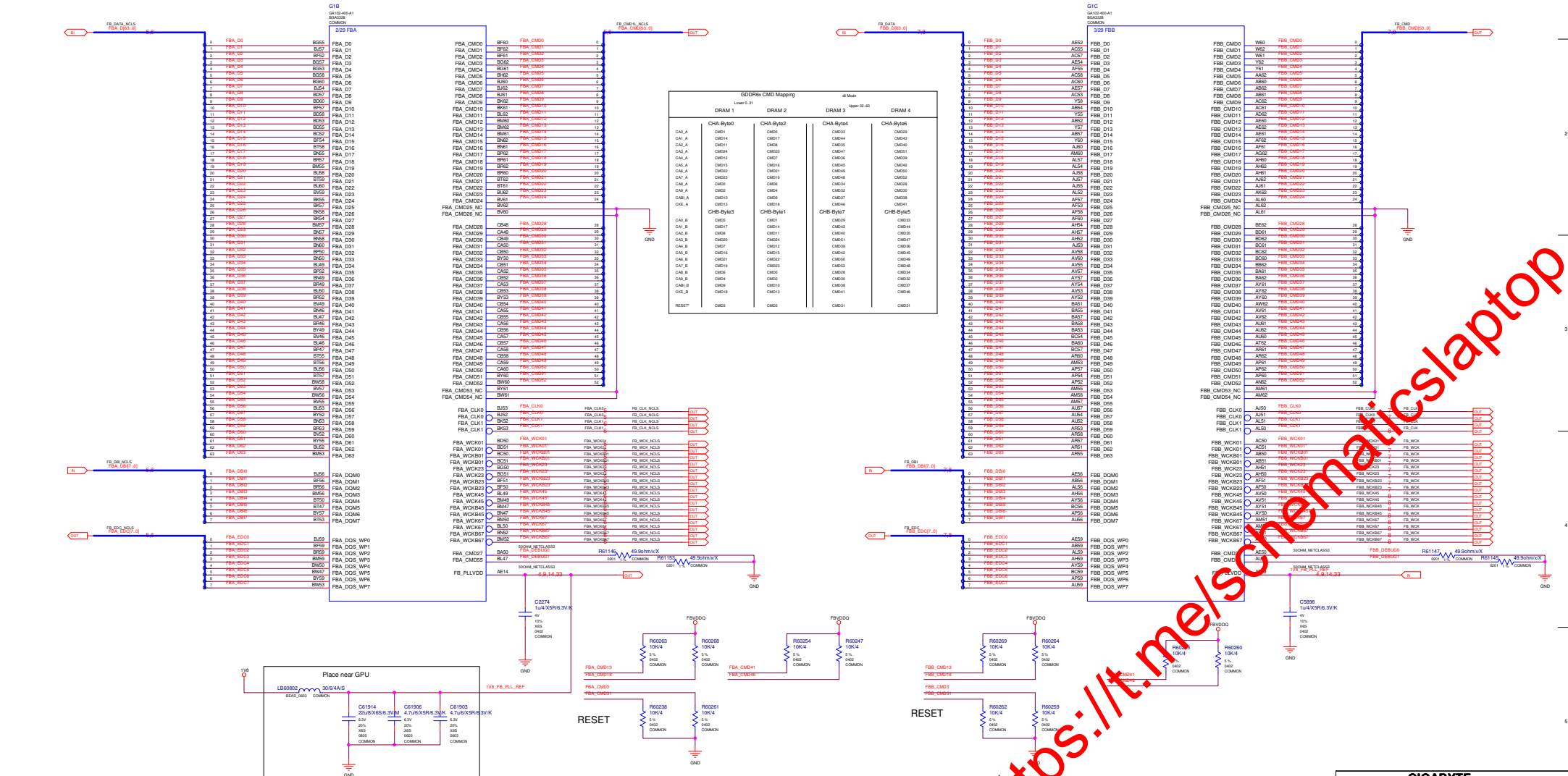
Page	Description	Page	Description	Page	Description
1	Table of Contents	26	MISC: THERMAL, JTAG, GPIO	51	PS: NVVDD Controller_OVR8
2	BLOCK DIAGRAM	27	IFPA UNUSED, IFPB UNUSED	52	PS: NVVDD PH1 (PWM1)
3	PCI EXPRESS	28	IFPE DP	53	PS: NVVDD PH2 (PWM6)
4	MEMORY: GPU PARTITION A/B	29	IFPD DP	54	PS: NVVDD PH3 (PWM3) and PH4 (PWM3)
5	MEMORY: FBA PARTITION[31:0]	30	IFPC HDMI/DP	55	PS: NVVDD PH5 (PWM7) and PH7 (PWM8)
6	MEMORY: FBA PARTITION[63:32]	31	IFPF DP	56	PS: NVVDD PH6 (PWM7)
7	MEMORY: FBB PARTITION[31:0]	32	MISC. ROM, STRAPS	57	PS: NVVDD PH8(PWM5) and PH9(PWM2)
8	MEMORY: FBB PARTITION[63:31]	33	MISC. XTAL, PLL	58	PS: NVVDD PH10 (PWM4)
9	MEMORY: GPU PARTITION C/D	34	PS: 5V	59	Colayout Notes
10	MEMORY: FBC PARTITION[31:0]	35	PS: PEX_DVDD and 1V8	60	BLANK
11	MEMORY: FBC PARTITION[63:32]	36	BLANK	61	PS: NVVDD OUTPUT CAP(TOP)
12	MEMORY: FBD PARTITION[31:0]	37	PS: FBVDD Controller OVR3	62	BLANK
13	MEMORY: FBD PARTITION[63:32]	38	PS: FBVDDQ OVR4	63	PS: INPUT SWITCH RTD3
14	MEMORY: GPU PARTITION E/F	39	PS: FBVDD PH1	64	BLANK
15	MEMORY: FBE PARTITION[31:0]	40	PS: FBVDD PH3	65	PS: INPUTS, FILTERING, and, MONITORING
16	MEMORY: FBE PARTITION[63:32]	41	PS: FBVDD PH2	66	PS: HOT UNPLUG
17	MEMORY: FBF PARTITION[31:0]	42	PS: FBVDD PH4	67	PS: Discrete Power Steering
18	MEMORY: FBF PARTITION[63:32]	43	PS: FBVDD OUTPUT CAP	68	PS: PREFILTER
19	GPU GND, RFUs & RSVD	44	PS: MSVDD CONTROLLER	69	PS: PREFILTER B
20	GPU POWERS	45	PS: MSVDD PH1	70	Sequence: 5V, 1V8, 3V3_SEQ
21	GPU: NVVDD DECOUPLING	46	PS: MSVDD PH2	71	Sequence: NV, PEX, FB EN
22	GPU: FBVDD DECOUPLING	47	PS: MSVDD PH3 and PH5	72	Sequence: 3V3 MONITOR
23	GPU: MSVDD DECOUPLING	48	PS: MSVDD PH4 and PH6	73	Sequence: MISC
24	BLANK	49	PS: MSVDD OUTPUT CAP(TOP)	74	MISC: LED & FAN
25	NVHS x16	50	BLANK	75	RGBW LED REF

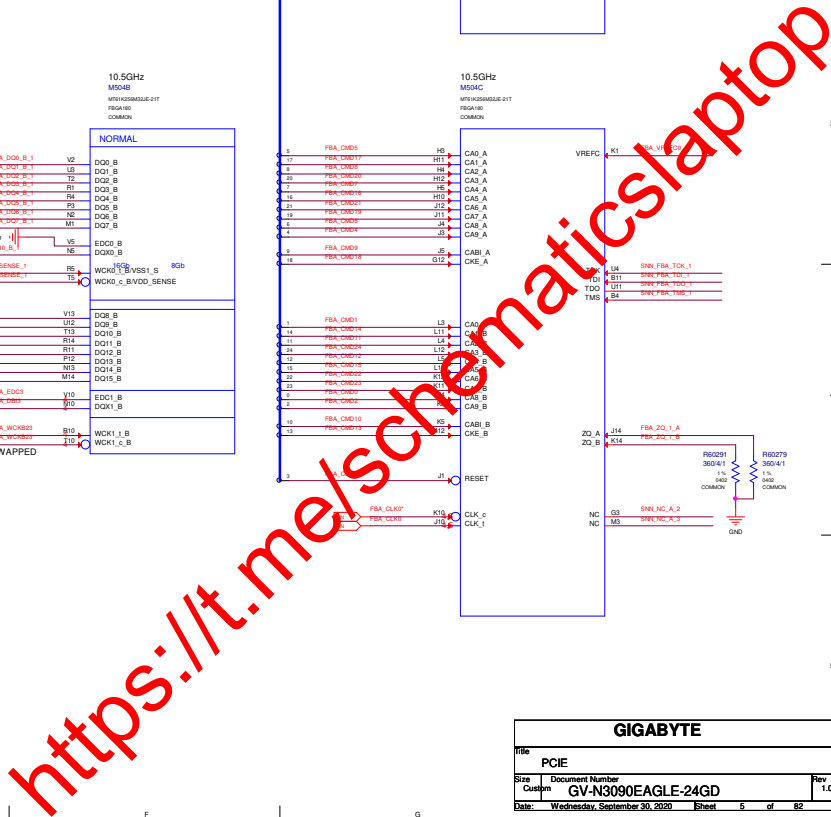
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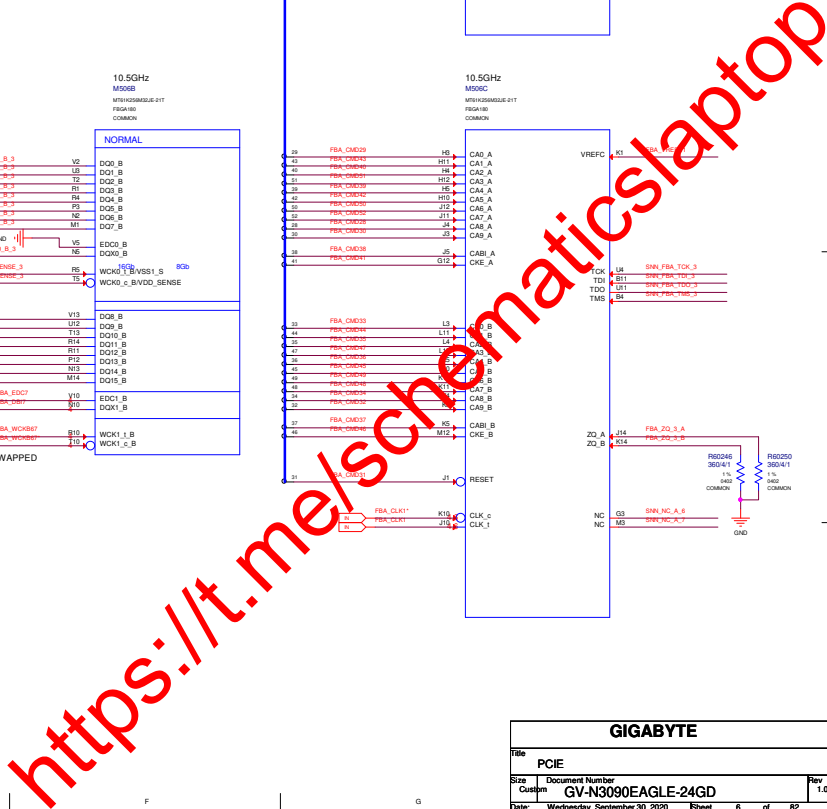
Block Diagram

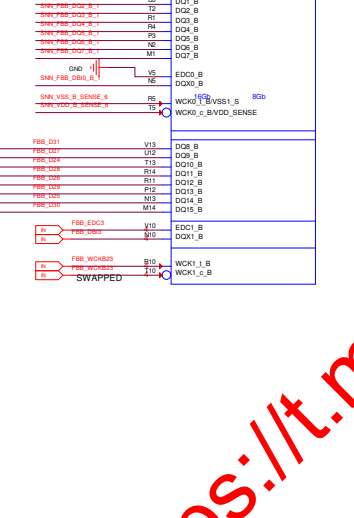
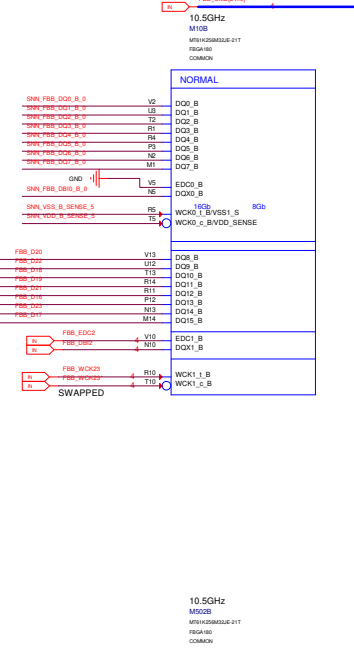
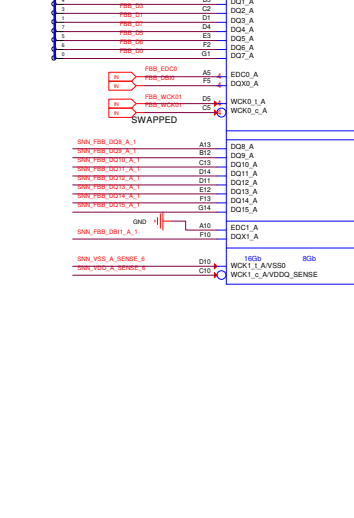
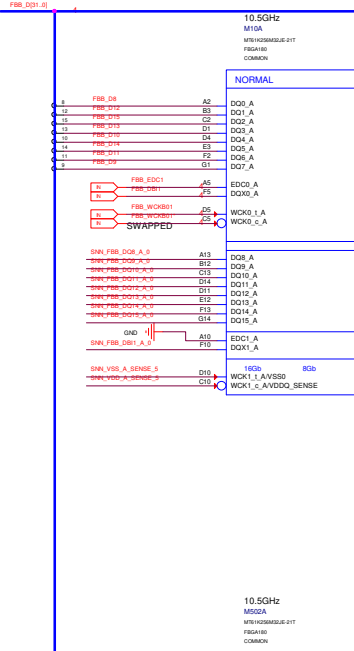
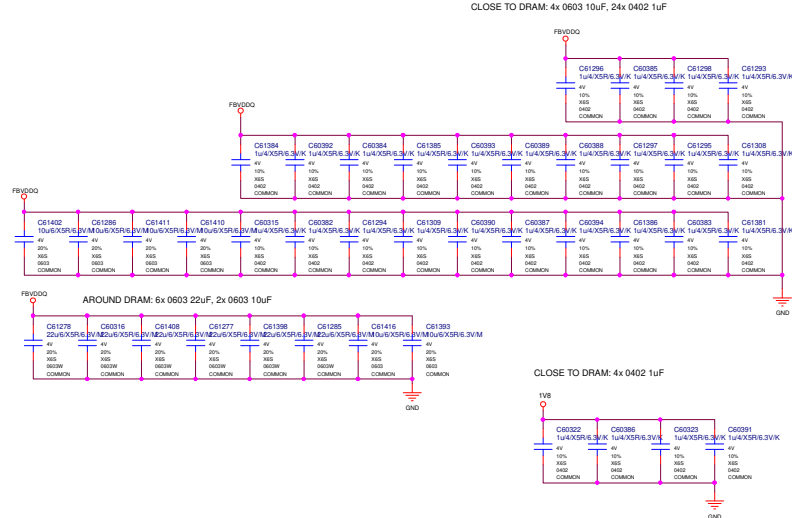
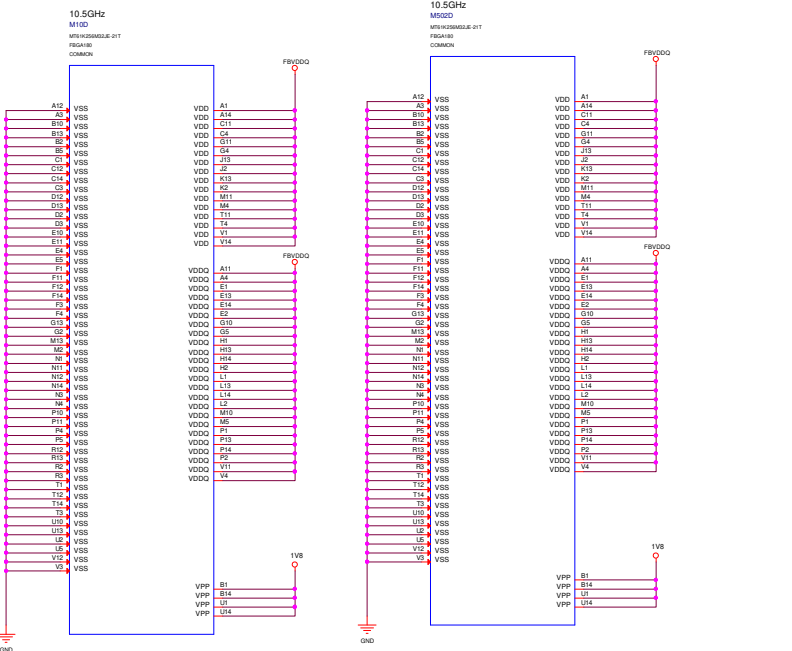


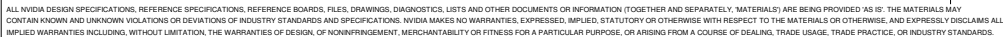


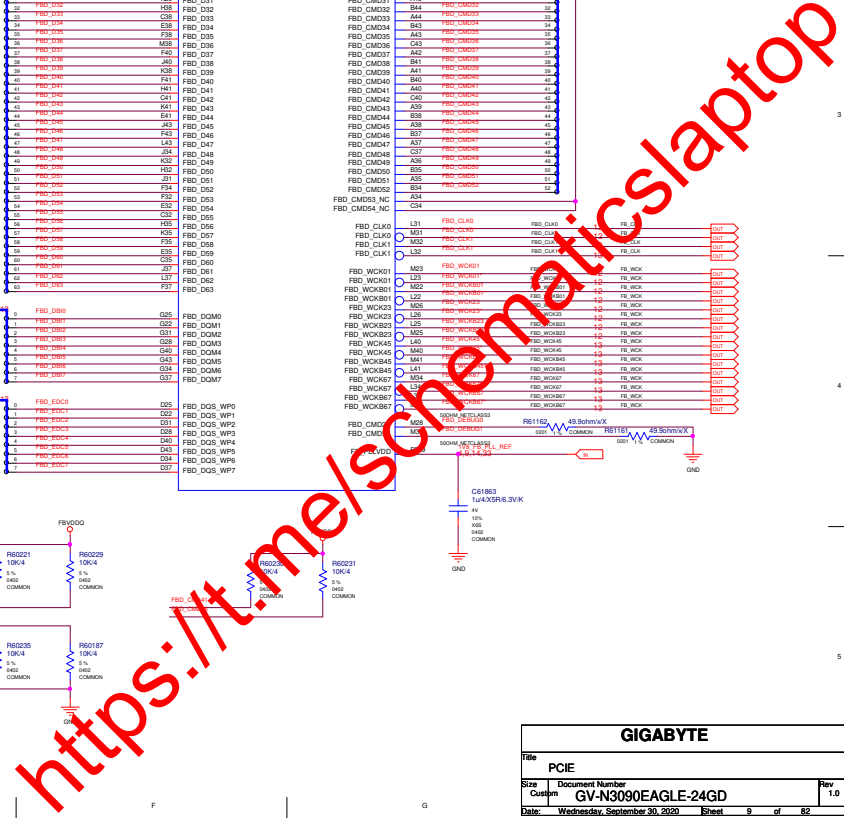


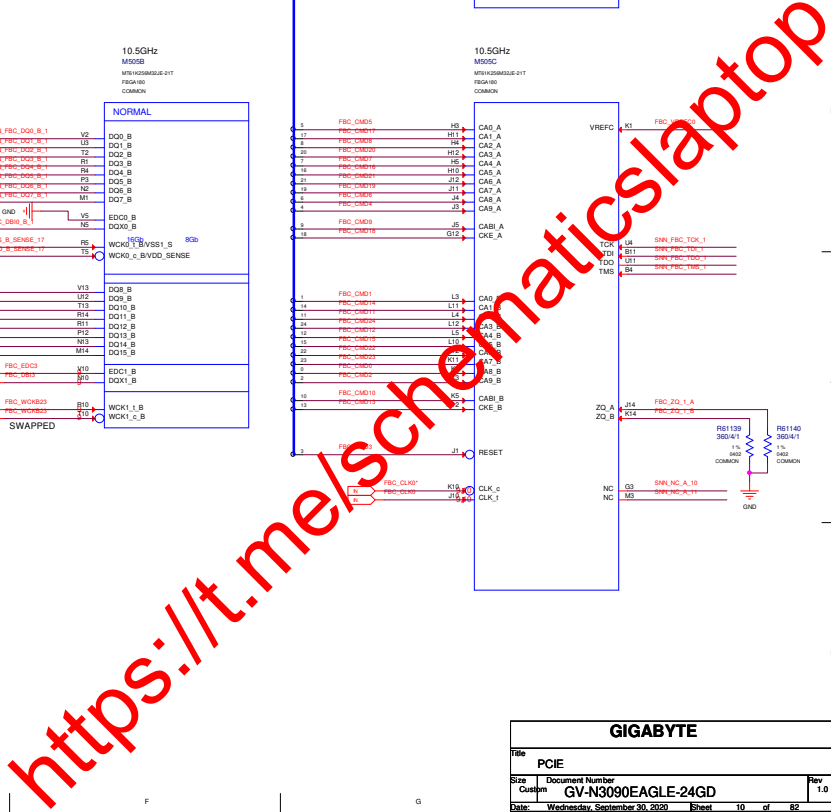


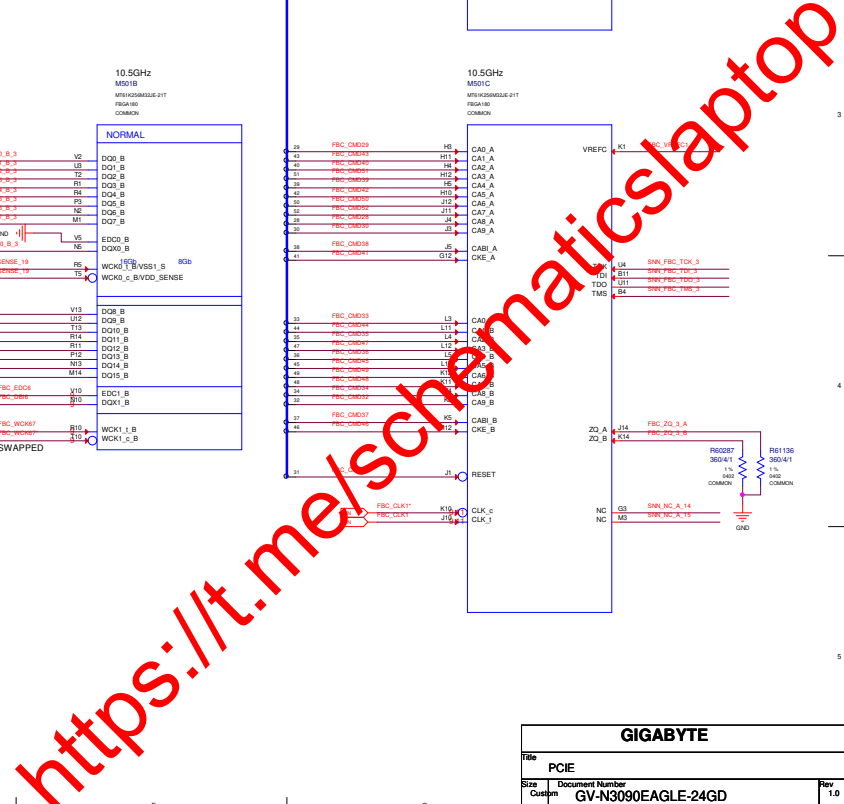


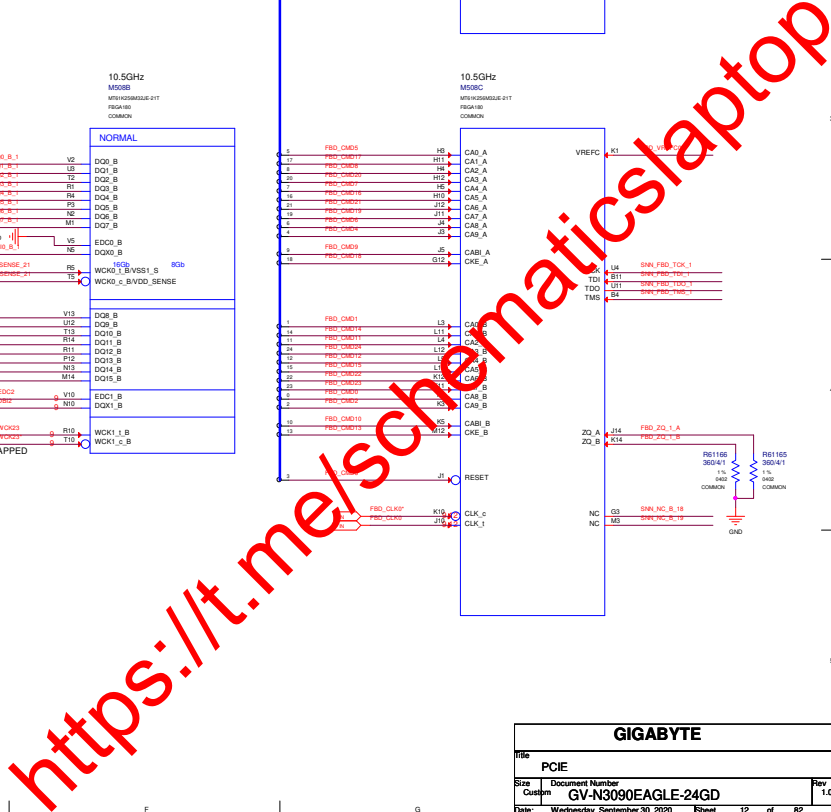


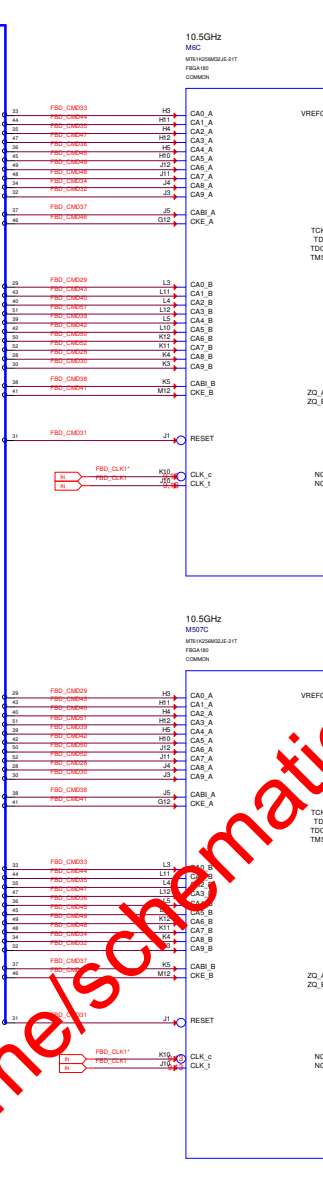


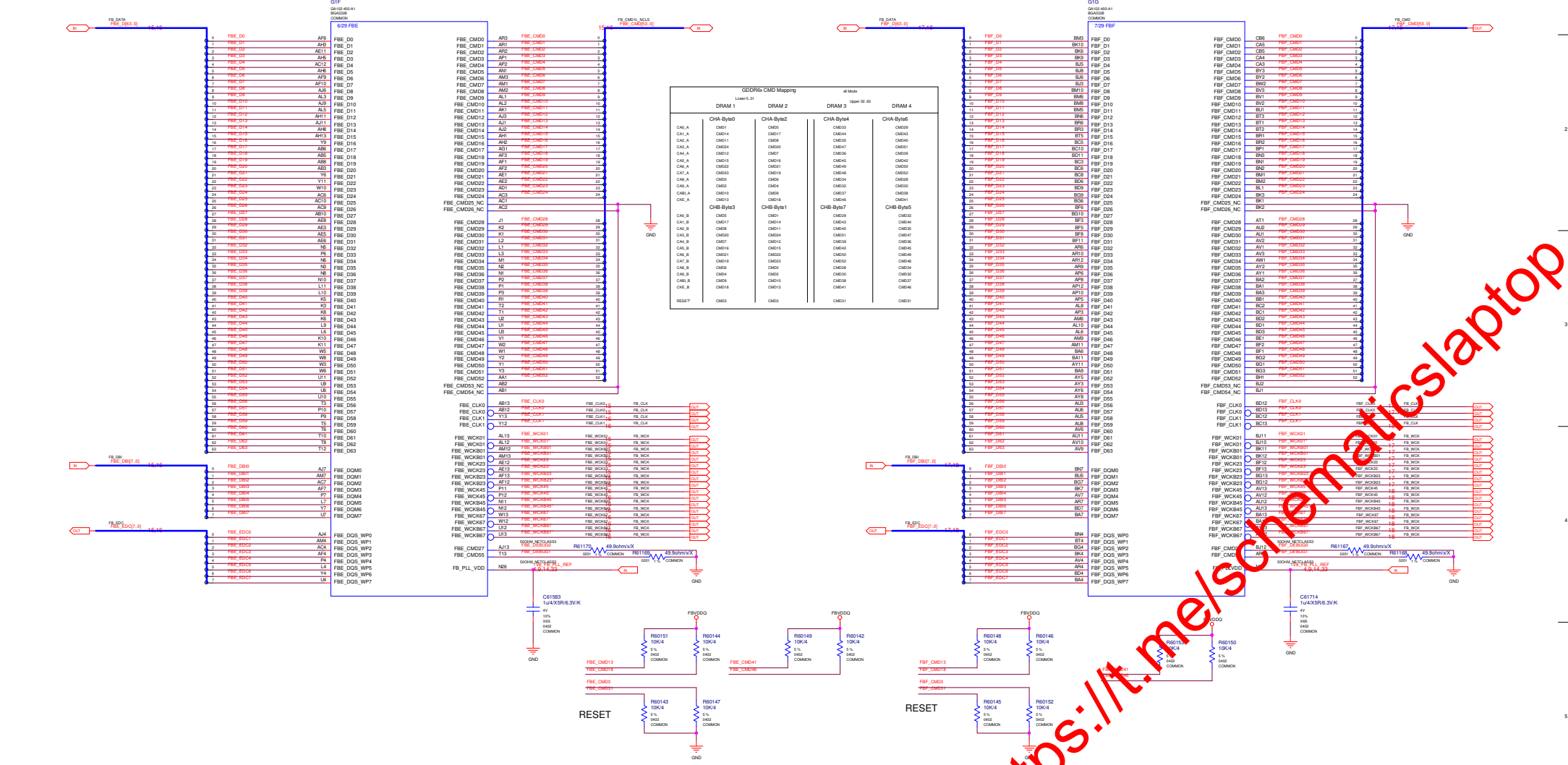


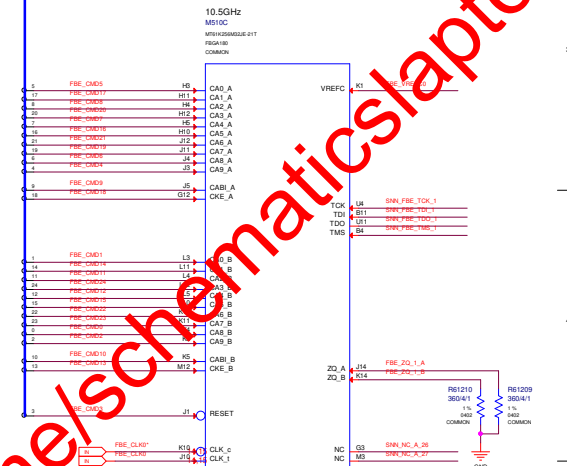
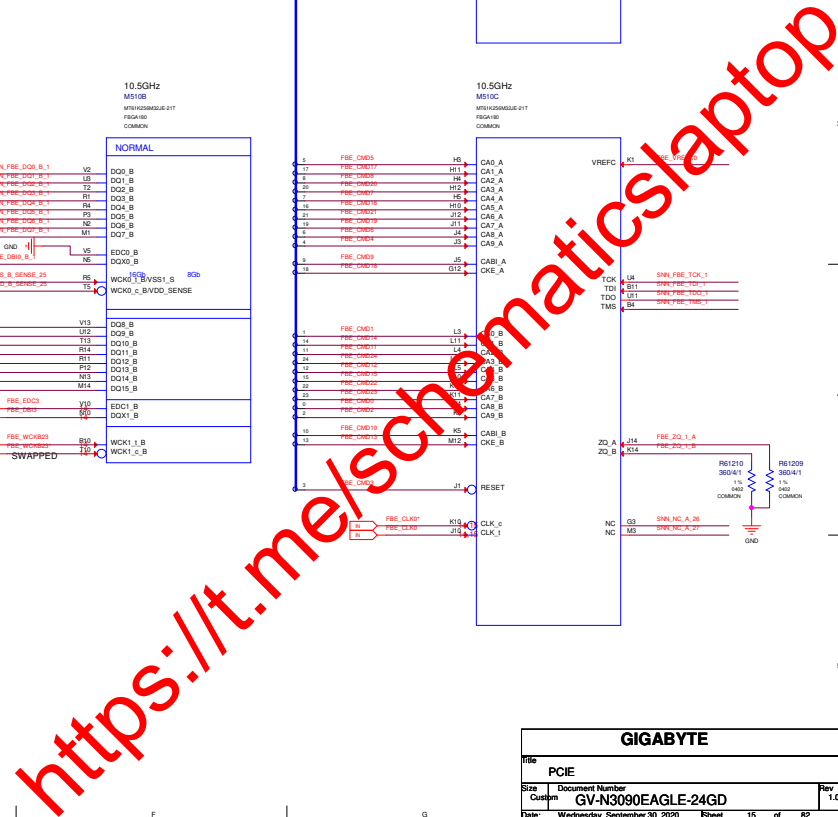


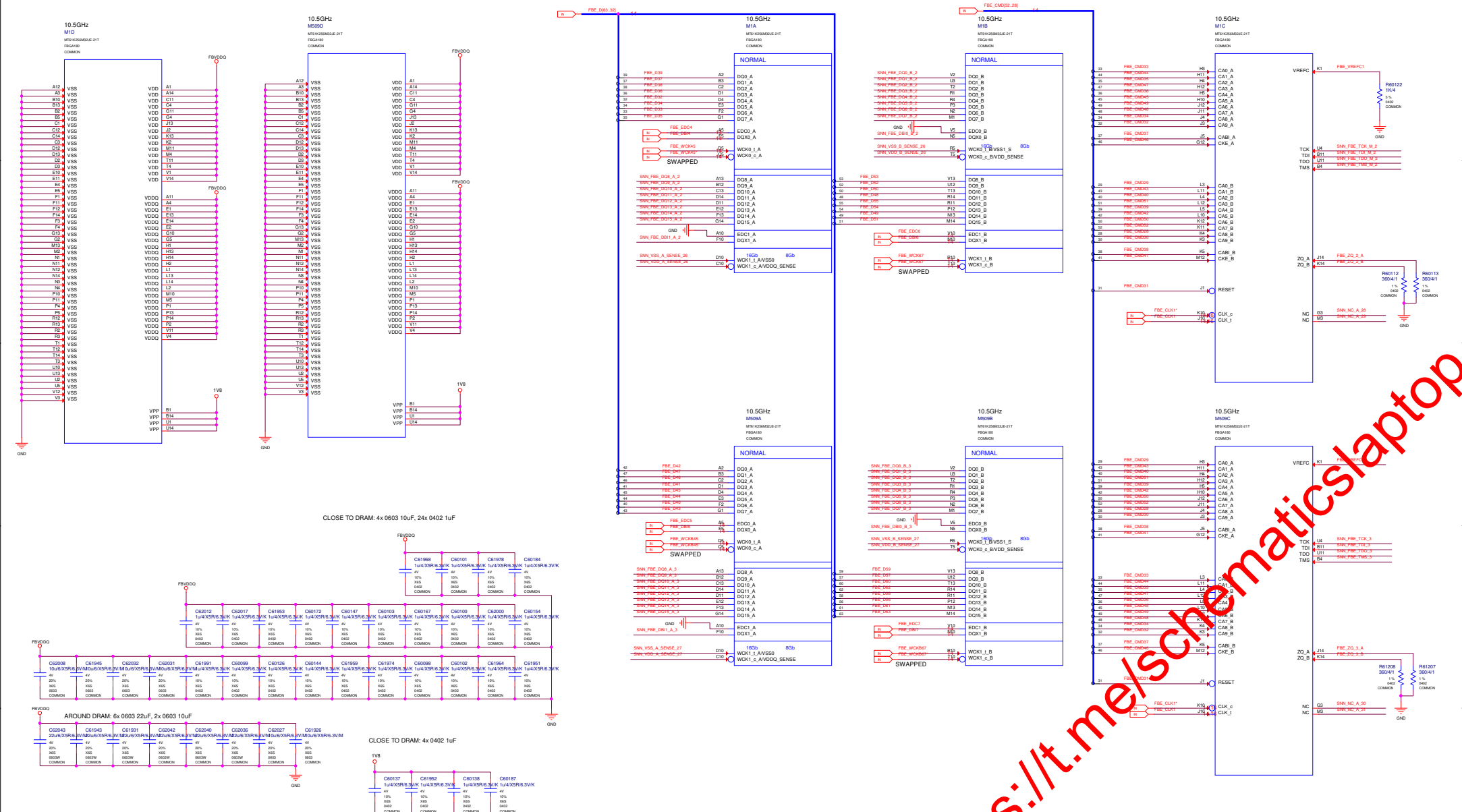


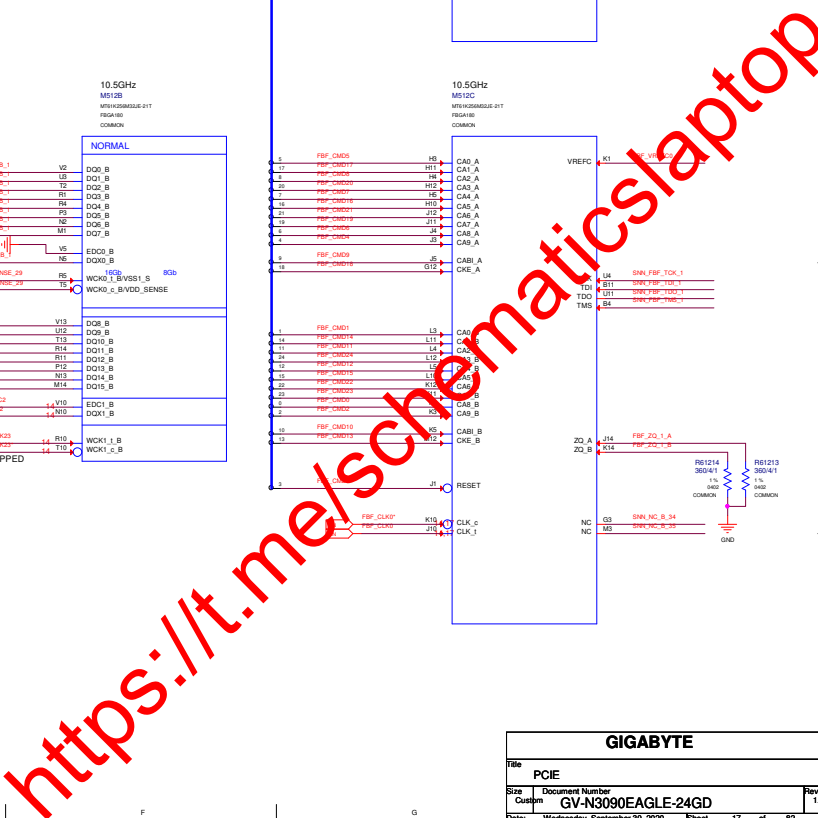


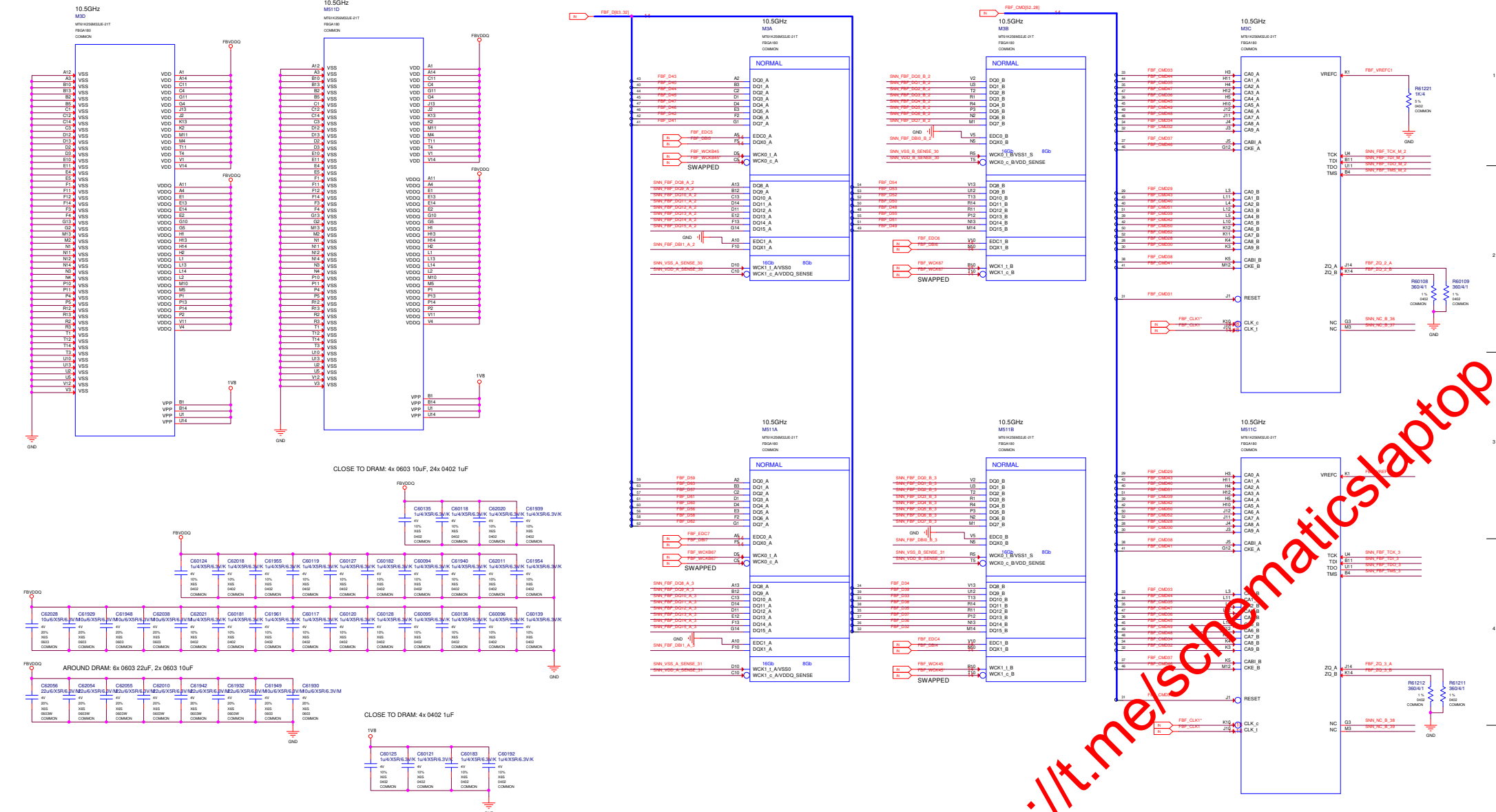




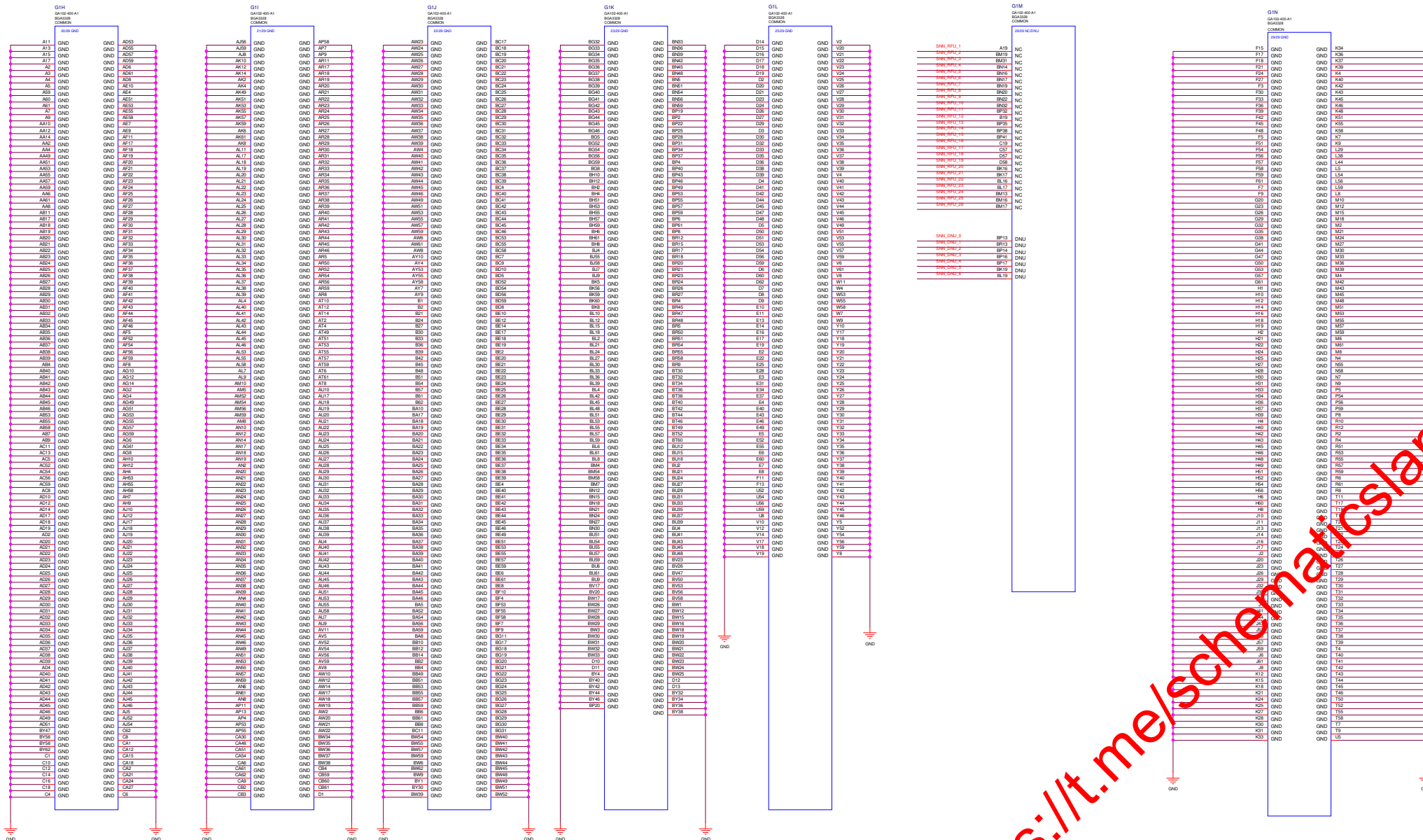








GPU GND, RFUs & RSVD

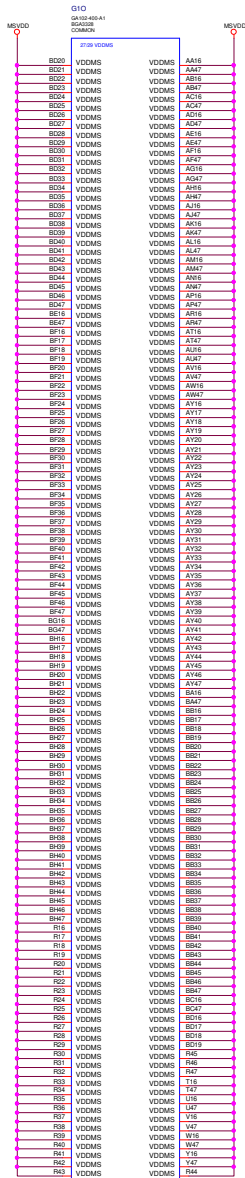
ASSEMBLY
PAGE DETAIL-ASSEMBLY DESCRIPTION-
GPU GND, RFUs & RSVD

GIGABYTE

File	PCIE
Docu	Document Number
GV-N3090E	GV-N3090E-24GD
Date	Wednesday, September 30, 2020
Sheet	19 of 82

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GPU PWR and GND

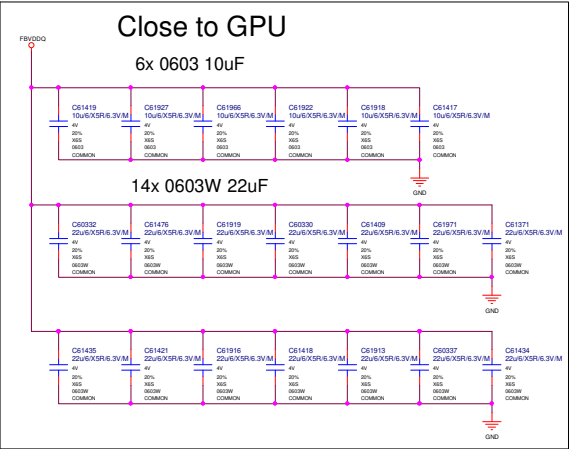
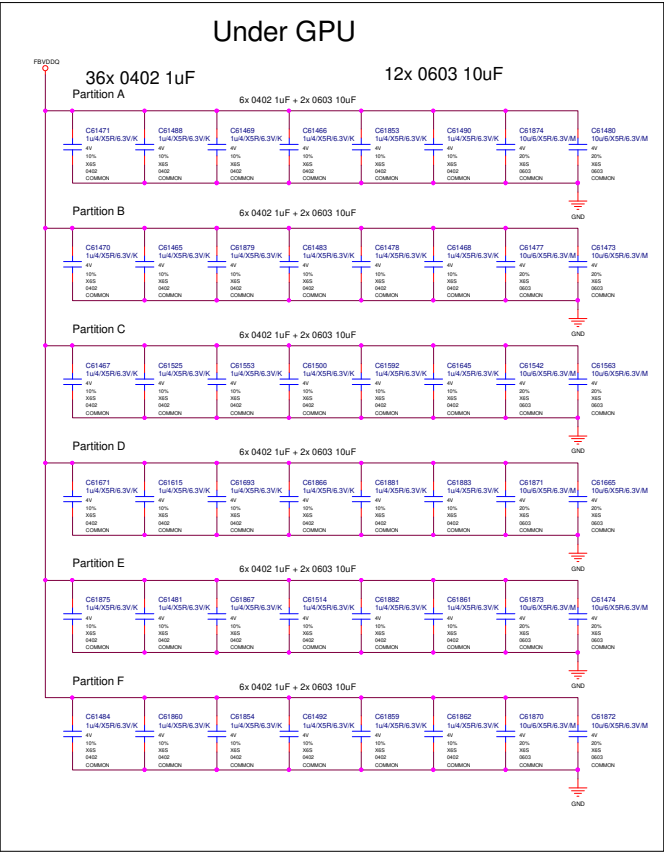


NVVDD UNDER GPU



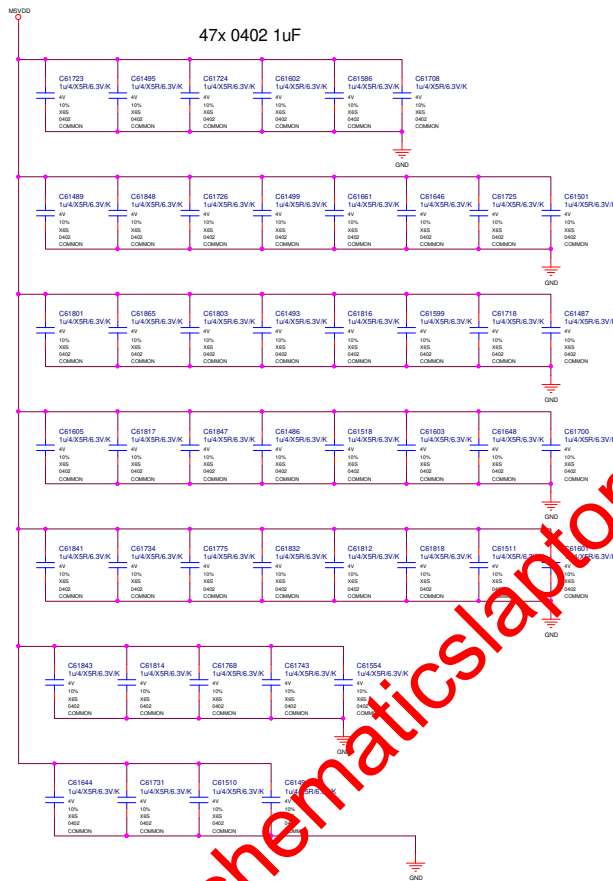
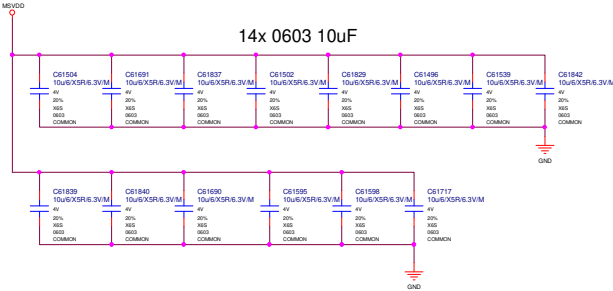
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FBVDDQ

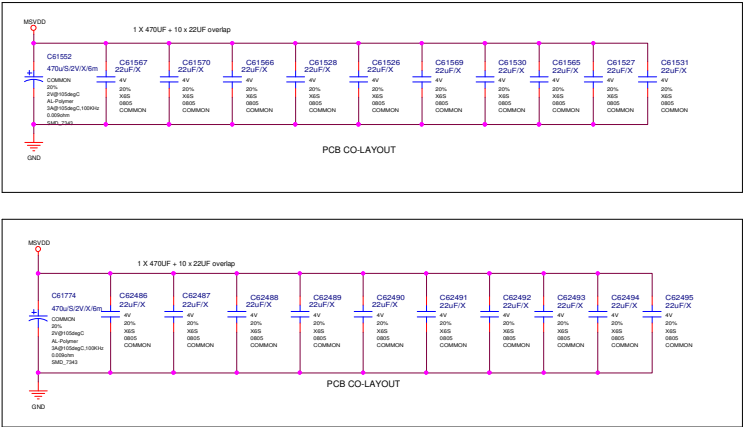


MSVDD

UNDER GPU



2x SMD7343 220uF (EACH CO-LAYOUT WITH 10x 22uF 0805)



REMOVED 3x 1uF 0603 DUE TO SPACE RESTRICTION

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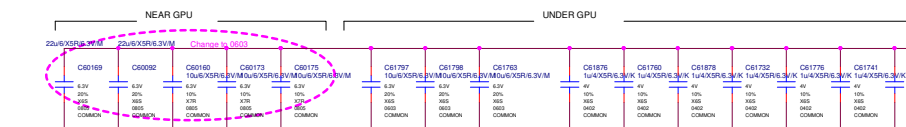
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ASSEMBLY
PAGE DETAIL

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GIGABYTE			
Title			
PCI-E			
Size	Document Number		Rev
Custom	GV-N3090EAGLE-24GD		1.0
Date:	Wednesday, September 30, 2020	Sheet	24 of 82

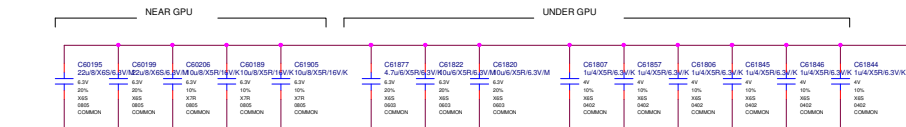
NVHS Interface and FRAME LOCK



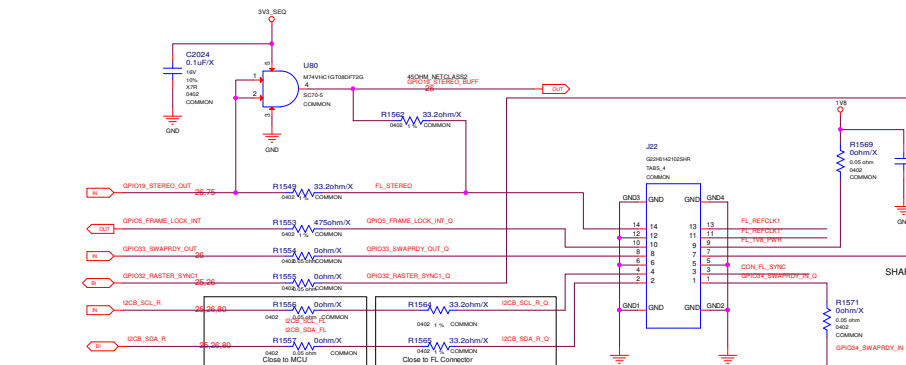
BRIDGE POR

LED -> NO

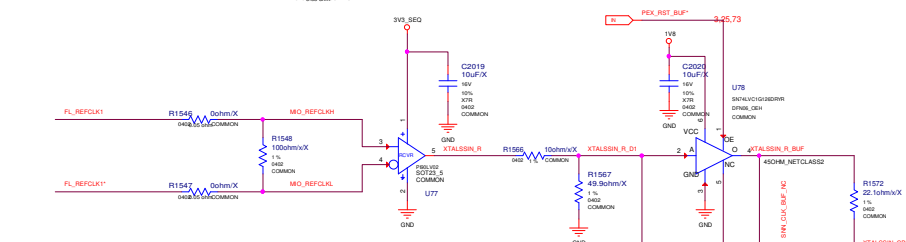
ID ROM ->YES



REMOVE ONE BECAUSE OF KO LIMITATION



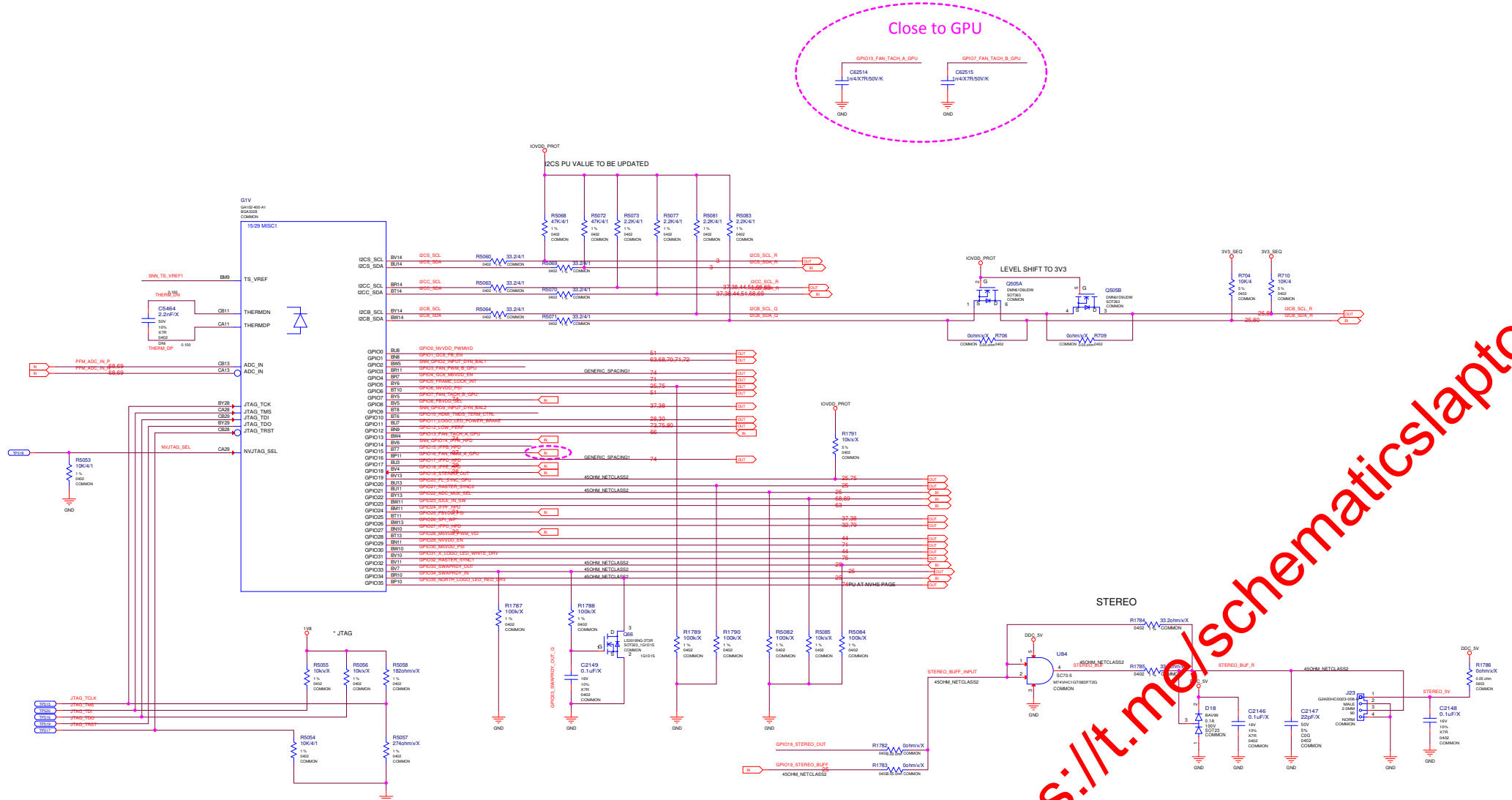
FRAMELOCK



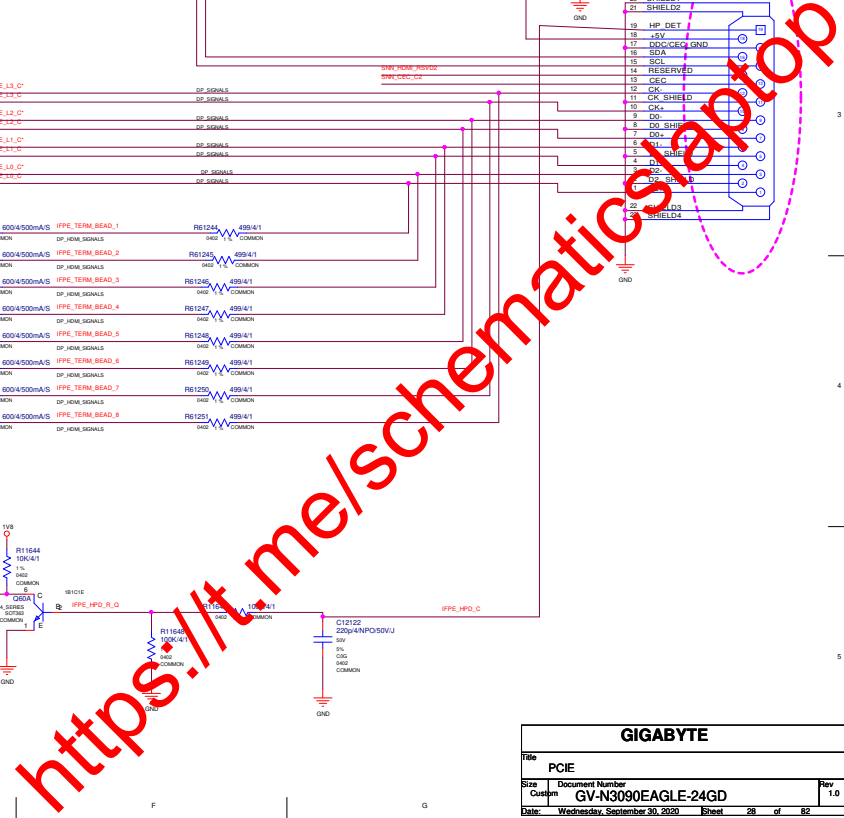
The schematic diagram illustrates the internal circuitry of the Gigabyte GA-N1000EAGLE-E-24GD PCIe card. Key components and connections include:

- PCIE Interface:** The card is connected to the PCIe bus via a 16-pin connector. The diagram shows the connection of the PCIe signals (P0, P1, P2, P3, P4, P5, P6, P7, P8, P9, P10, P11, P12, P13, P14, P15) to the N1000EAGLE-E-24GD chip.
- Power and Ground:** The card is powered by a 3.3V regulator (R1000) and a 1.8V regulator (R1001). Ground connections are shown for various pins and components.
- Resistors and Capacitors:** Various resistors (R1000, R1001, R1002, R1003, R1004, R1005, R1006, R1007, R1008, R1009, R1010, R1011, R1012, R1013, R1014, R1015, R1016, R1017, R1018, R1019, R1020, R1021, R1022, R1023, R1024, R1025, R1026, R1027, R1028, R1029, R1030, R1031, R1032, R1033, R1034, R1035, R1036, R1037, R1038, R1039, R1040, R1041, R1042, R1043, R1044, R1045, R1046, R1047, R1048, R1049, R1050, R1051, R1052, R1053, R1054, R1055, R1056, R1057, R1058, R1059, R1060, R1061, R1062, R1063, R1064, R1065, R1066, R1067, R1068, R1069, R1070, R1071, R1072, R1073, R1074, R1075, R1076, R1077, R1078, R1079, R1080, R1081, R1082, R1083, R1084, R1085, R1086, R1087, R1088, R1089, R1090, R1091, R1092, R1093, R1094, R1095, R1096, R1097, R1098, R1099, R1100, R1101, R1102, R1103, R1104, R1105, R1106, R1107, R1108, R1109, R1110, R1111, R1112, R1113, R1114, R1115, R1116, R1117, R1118, R1119, R1120, R1121, R1122, R1123, R1124, R1125, R1126, R1127, R1128, R1129, R1130, R1131, R1132, R1133, R1134, R1135, R1136, R1137, R1138, R1139, R1140, R1141, R1142, R1143, R1144, R1145, R1146, R1147, R1148, R1149, R1150, R1151, R1152, R1153, R1154, R1155, R1156, R1157, R1158, R1159, R1160, R1161, R1162, R1163, R1164, R1165, R1166, R1167, R1168, R1169, R1170, R1171, R1172, R1173, R1174, R1175, R1176, R1177, R1178, R1179, R1180, R1181, R1182, R1183, R1184, R1185, R1186, R1187, R1188, R1189, R1190, R1191, R1192, R1193, R1194, R1195, R1196, R1197, R1198, R1199, R1200, R1201, R1202, R1203, R1204, R1205, R1206, R1207, R1208, R1209, R1210, R1211, R1212, R1213, R1214, R1215, R1216, R1217, R1218, R1219, R1220, R1221, R1222, R1223, R1224, R1225, R1226, R1227, R1228, R1229, R1230, R1231, R1232, R1233, R1234, R1235, R1236, R1237, R1238, R1239, R1240, R1241, R1242, R1243, R1244, R1245, R1246, R1247, R1248, R1249, R1250, R1251, R1252, R1253, R1254, R1255, R1256, R1257, R1258, R1259, R1260, R1261, R1262, R1263, R1264, R1265, R1266, R1267, R1268, R1269, R1270, R1271, R1272, R1273, R1274, R1275, R1276, R1277, R1278, R1279, R1280, R1281, R1282, R1283, R1284, R1285, R1286, R1287, R1288, R1289, R1290, R1291, R1292, R1293, R1294, R1295, R1296, R1297, R1298, R1299, R1300, R1301, R1302, R1303, R1304, R1305, R1306, R1307, R1308, R1309, R1310, R1311, R1312, R1313, R1314, R1315, R1316, R1317, R1318, R1319, R1320, R1321, R1322, R1323, R1324, R1325, R1326, R1327, R1328, R1329, R1330, R1331, R1332, R1333, R1334, R1335, R1336, R1337, R1338, R1339, R1340, R1341, R1342, R1343, R1344, R1345, R1346, R1347, R1348, R1349, R1350, R1351, R1352, R1353, R1354, R1355, R1356, R1357, R1358, R1359, R1360, R1361, R1362, R1363, R1364, R1365, R1366, R1367, R1368, R1369, R1370, R1371, R1372, R1373, R1374, R1375, R1376, R1377, R1378, R1379, R1380, R1381, R1382, R1383, R1384, R1385, R1386, R1387, R1388, R1389, R1390, R1391, R1392, R1393, R1394, R1395, R1396, R1397, R1398, R1399, R1400, R1401, R1402, R1403, R1404, R1405, R1406, R1407, R1408, R1409, R1410, R1411, R1412, R1413, R1414, R1415, R1416, R1417, R1418, R1419, R1420, R1421, R1422, R1423, R1424, R1425, R1426, R1427, R1428, R1429, R1430, R1431, R1432, R1433, R1434, R1435, R1436, R1437, R1438, R1439, R1440, R1441, R1442, R1443, R1444, R1445, R1446, R1447, R1448, R1449, R1450, R1451, R1452, R1453, R1454, R1455, R1456, R1457, R1458, R1459, R1460, R1461, R1462, R1463, R1464, R1465, R1466, R1467, R1468, R1469, R1470, R1471, R1472, R1473, R1474, R1475, R1476, R1477, R1478, R1479, R1480, R1481, R1482, R1483, R1484, R1485, R1486, R1487, R1488, R1489, R1490, R1491, R1492, R1493, R1494, R1495, R1496, R1497, R1498, R1499, R1500, R1501, R1502, R1503, R1504, R1505, R1506, R1507, R1508, R1509, R1510, R1511, R1512, R1513, R1514, R1515, R1516, R1517, R1518, R1519, R1520, R1521, R1522, R1523, R1524, R1525, R1526, R1527, R1528, R1529, R1530, R1531, R1532, R1533, R1534, R1535, R1536, R1537, R1538, R1539, R1540, R1541, R1542, R1543, R1544, R1545, R1546, R1547, R1548, R1549, R1550, R1551, R1552, R1553, R1554, R1555, R1556, R1557, R1558, R1559, R1560, R1561, R1562, R1563, R1564, R1565, R1566, R1567, R1568, R1569, R1570, R1571, R1572, R1573, R1574, R1575, R1576, R1577, R1578, R1579, R1580, R1581, R1582, R1583, R1584, R1585, R1586, R1587, R1588, R1589, R1590, R1591, R1592, R1593, R1594, R1595, R1596, R1597, R1598, R1599, R1600, R1601, R1602, R1603, R1604, R1605, R1606, R1607, R1608, R1609, R1610, R1611, R1612, R1613, R1614, R1615, R1616, R1617, R1618, R1619, R1620, R1621, R1622, R1623, R1624, R1625, R16

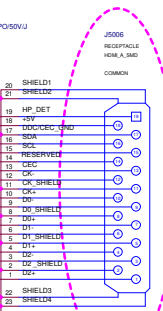
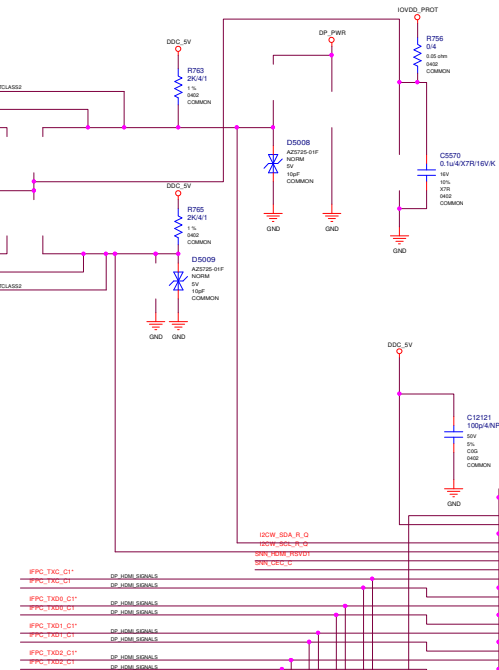
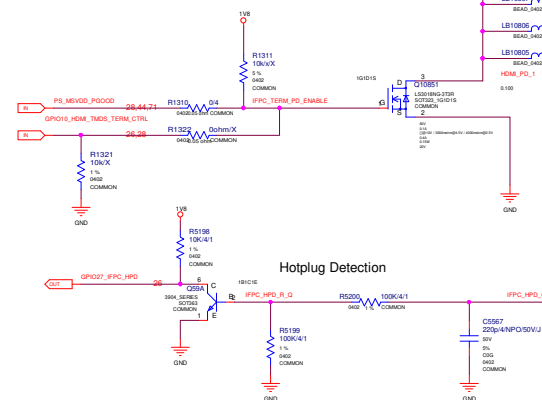
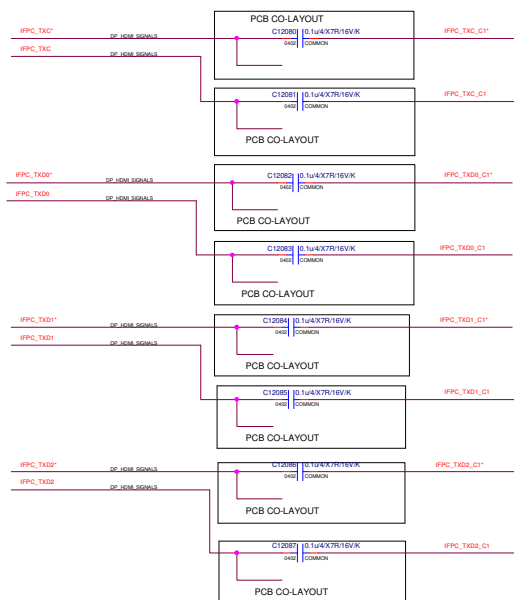
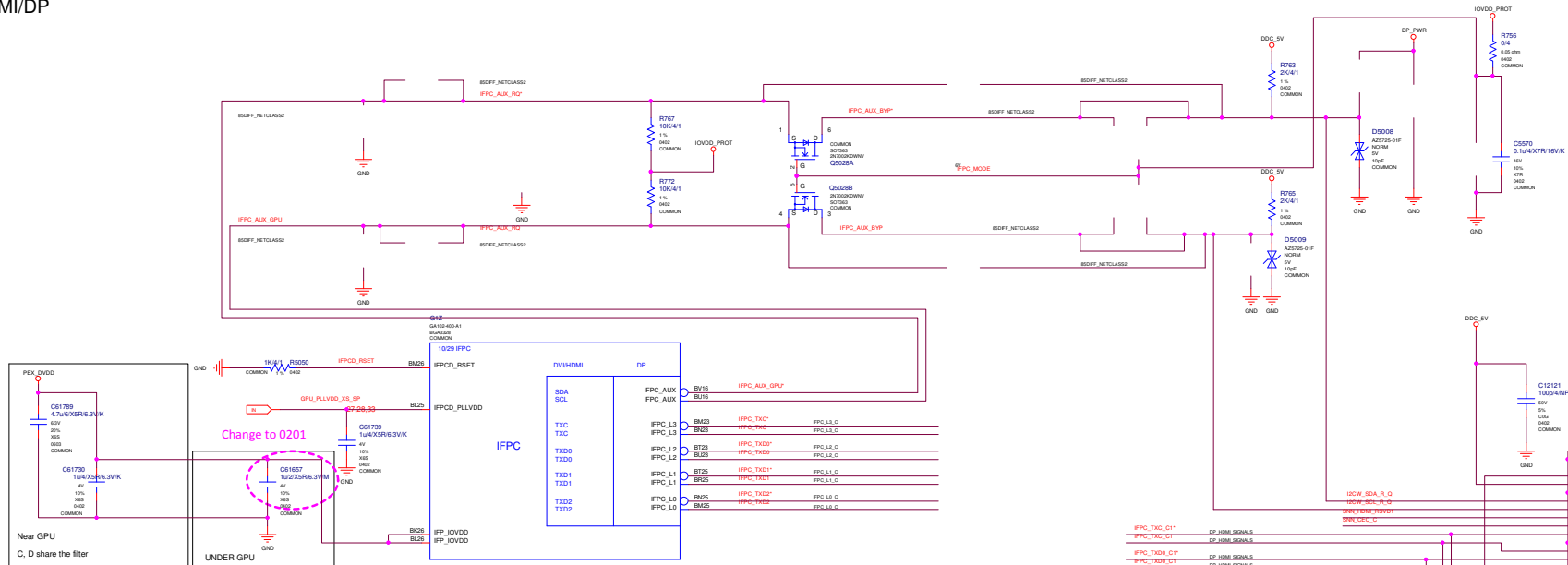
MISC1: Fan, Thermal, JTAG, GPIO, STEREO



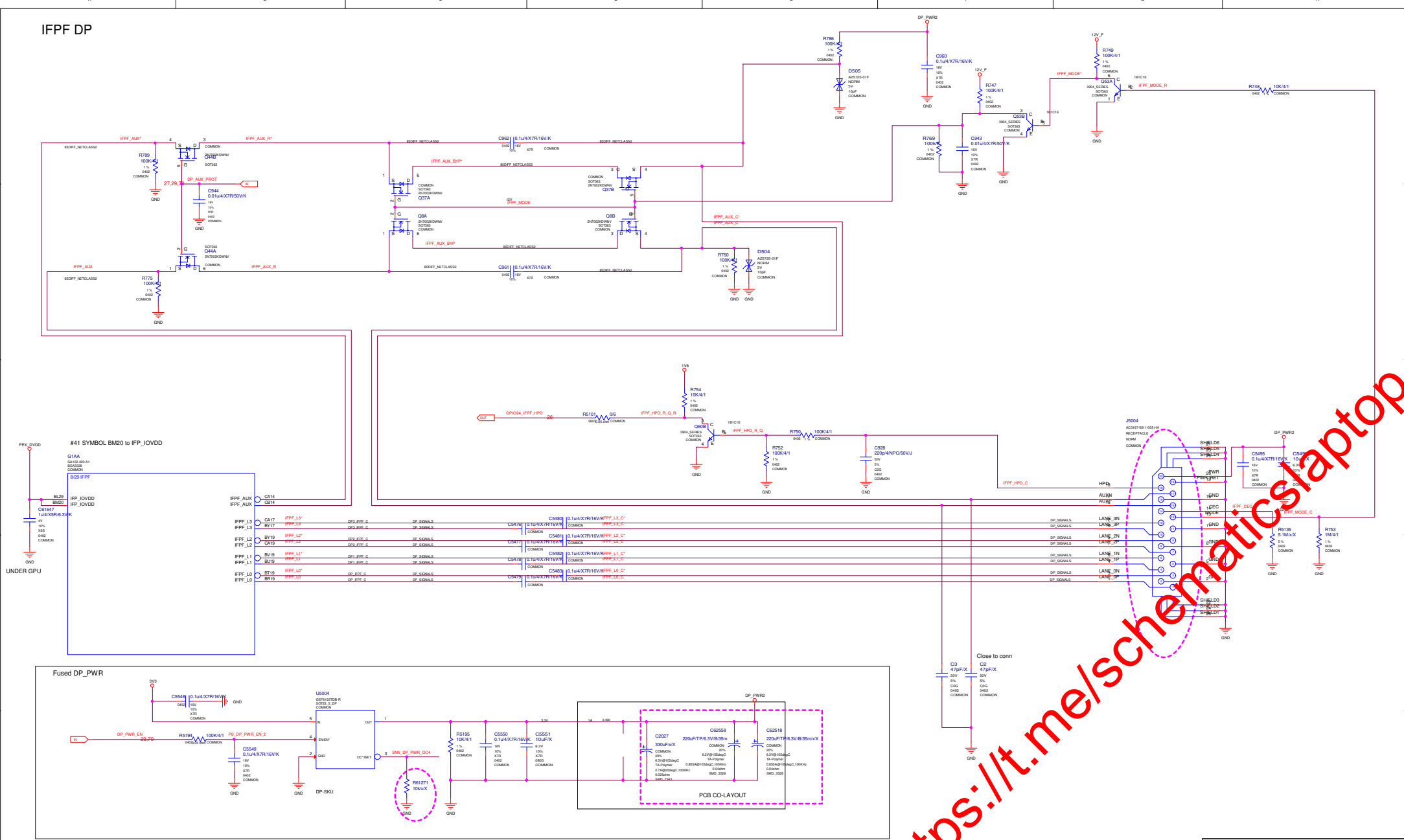
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IFPC HDMI/DP

[illegible]

IFPF DP



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MISC: ROM, Straps

STRAP2	STRAP1	STRAP0	RAMCFG[4:0]	
L	L	L	00000	RAMCFG MICRON 8Gb 19Gbps
L	H	L	00010	RAMCFG TBD
H	L	L	00100	RAMCFG TBD
H	L	H	00101	RAMCFG TBD
H	L	H	00101	RAMCFG MICRON 8Gb 21Gbps

H=High :Tied to 1.8V
M=Middle:Tied to 0.9V
L=Low :Tied to 0V

DEFAULT

ROM_SO	ROM_SI	ROM_SCLK	SMARTFAN[2:0].FS_OVERT	1:ENABLE 0:DISABLE
H	H	H	0111	FS_OVERT ENABLE
L	L	L	0000	FS_OVERT DISABLE

DEFAULT

STRAP5	STRAP4	STRAP3	SMB_ALT_ADDR	DEVID_SEL	PCIE_CFG	VGA_DEVICE
M	H	H	1	1	1	1
M	H	L	1	H	1	0
M	L	H	1	1	0	1
M	L	L	1	1	0	0
L	H	M	1	0	1	1
L	M	H	1	0	1	0
L	M	L	1	0	0	1
L	L	M	1	0	0	0
H	H	H	0	1	1	1
H	H	L	0	1	1	0
H	L	H	0	1	0	1
H	L	L	0	1	0	0
L	H	H	0	0	1	1
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L	L	H	0	0	0	1
L	L	L	0	0	0	0

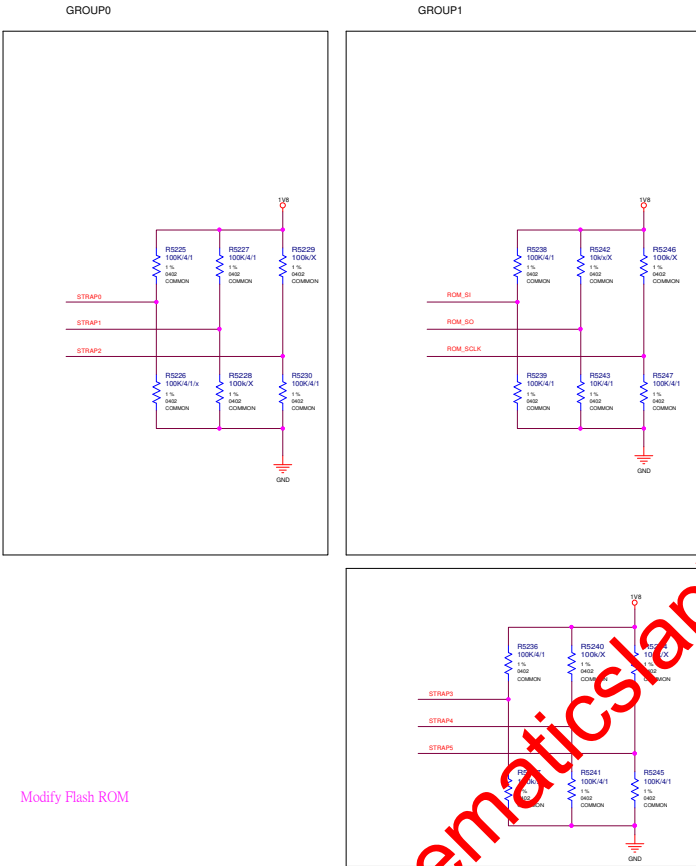
1:SMB_ALT_ADDR ENABLE
0:SMB_ALT_ADDR DISABLE

1:DEVID_SEL REBRAND
0:DEVID_SEL ORIGINAL

1:PCIE_CFG LOW POWER
0:PCIE_CFG HIGH POWER

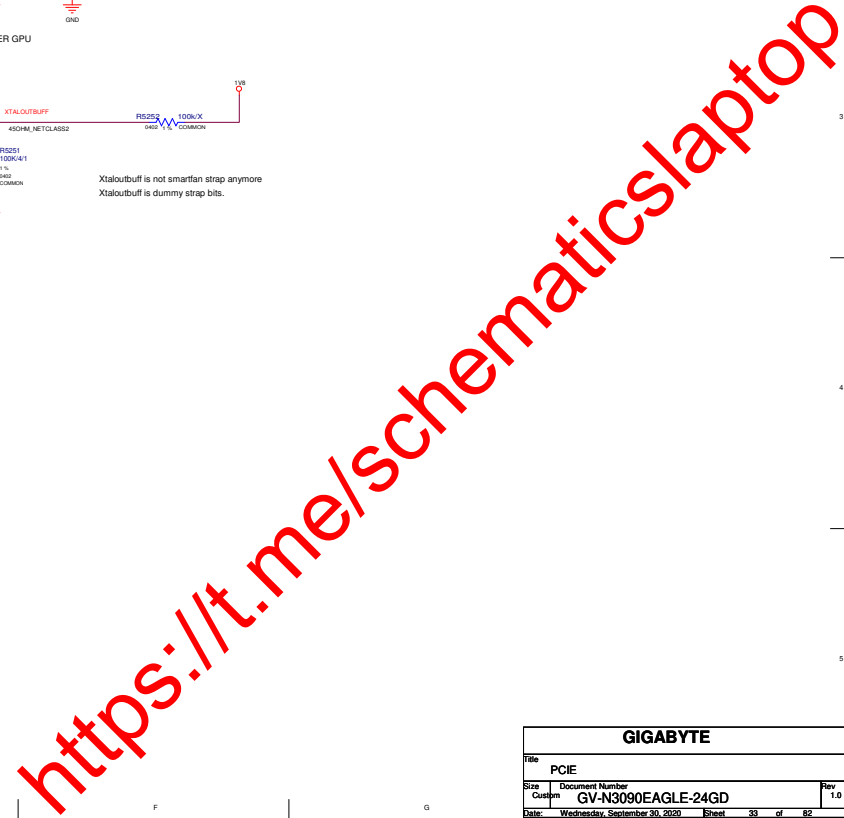
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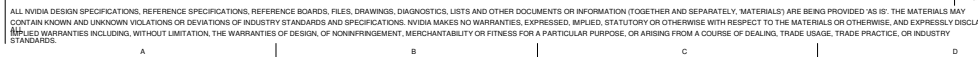
Default



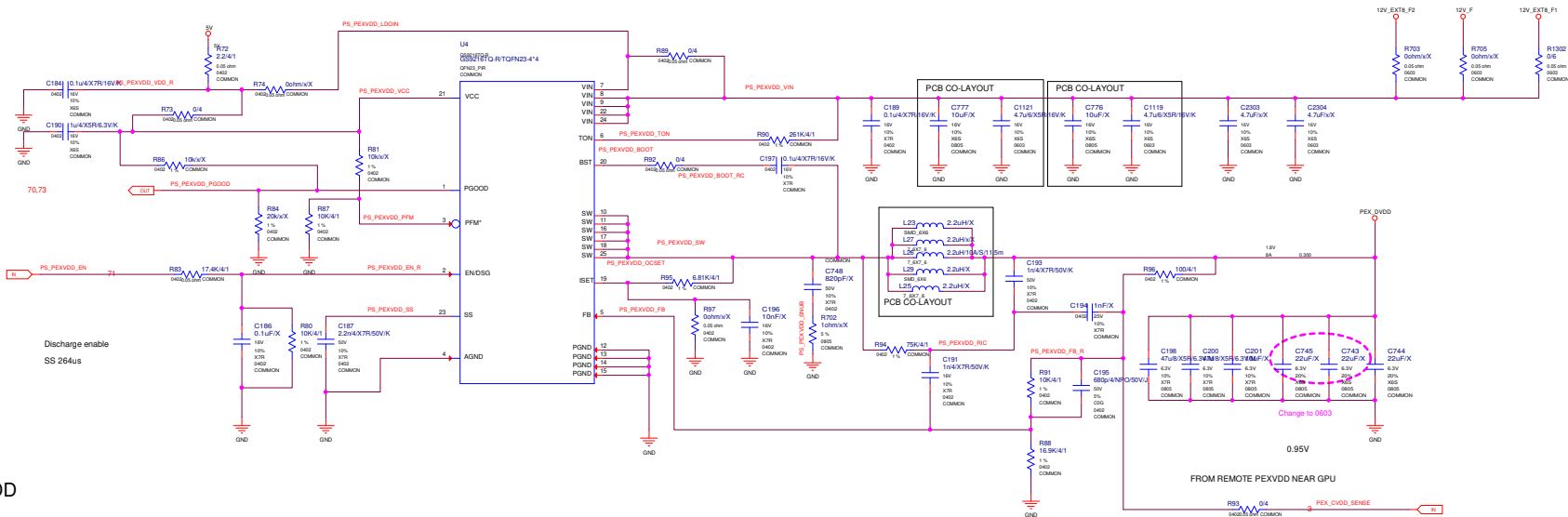
Modify Flash ROM



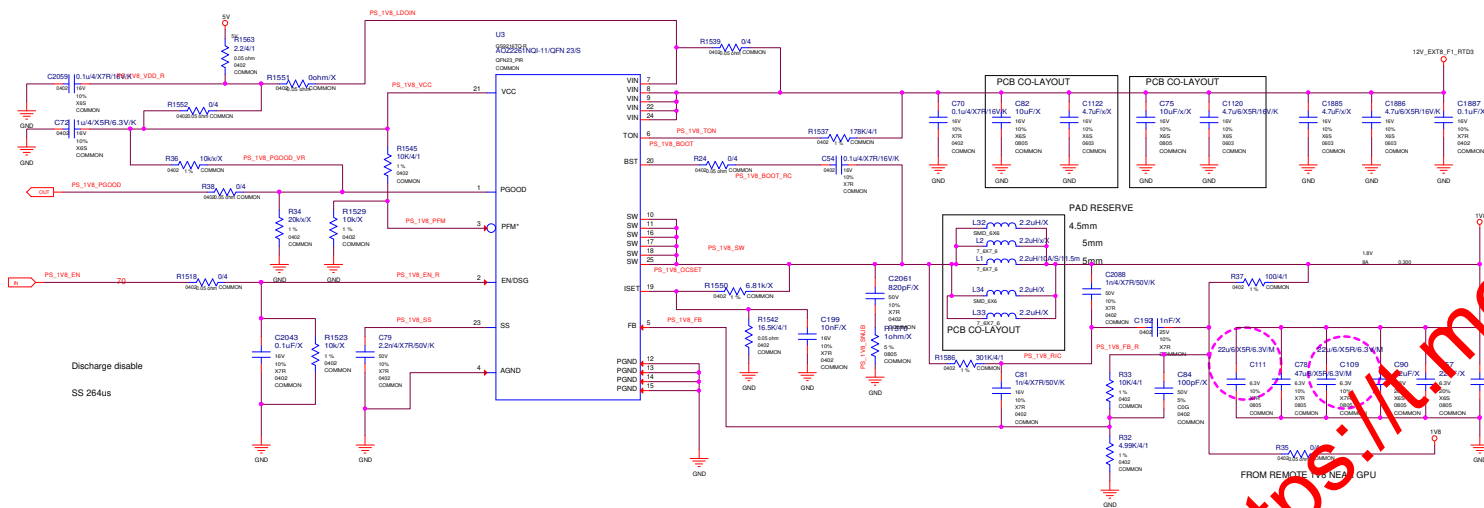




PS: PEX DVDD 1V8 Rail



PEX DVDD



1V8

BLANK

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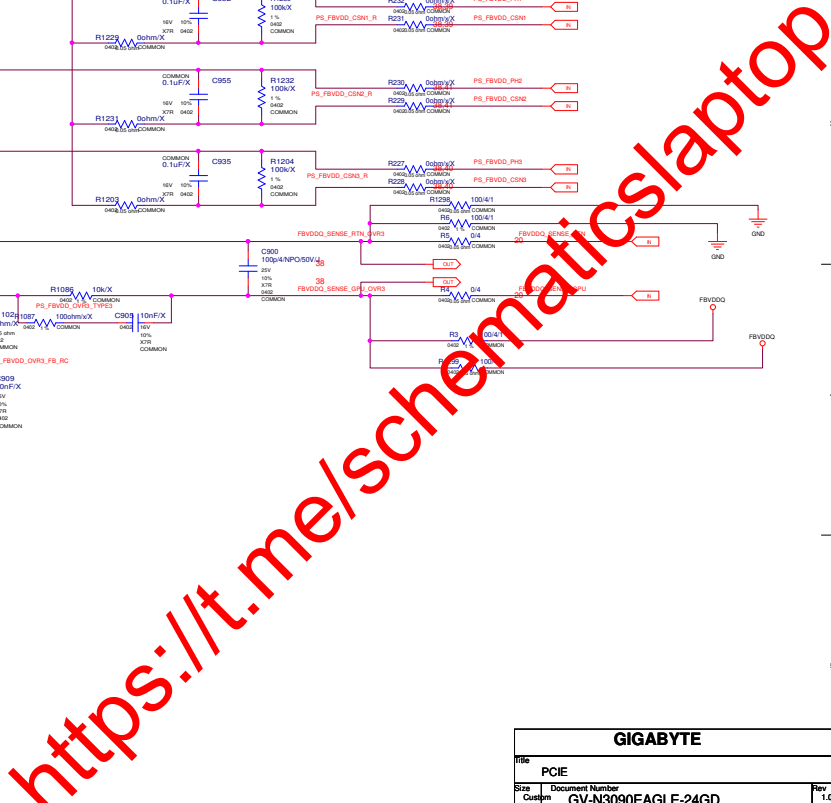
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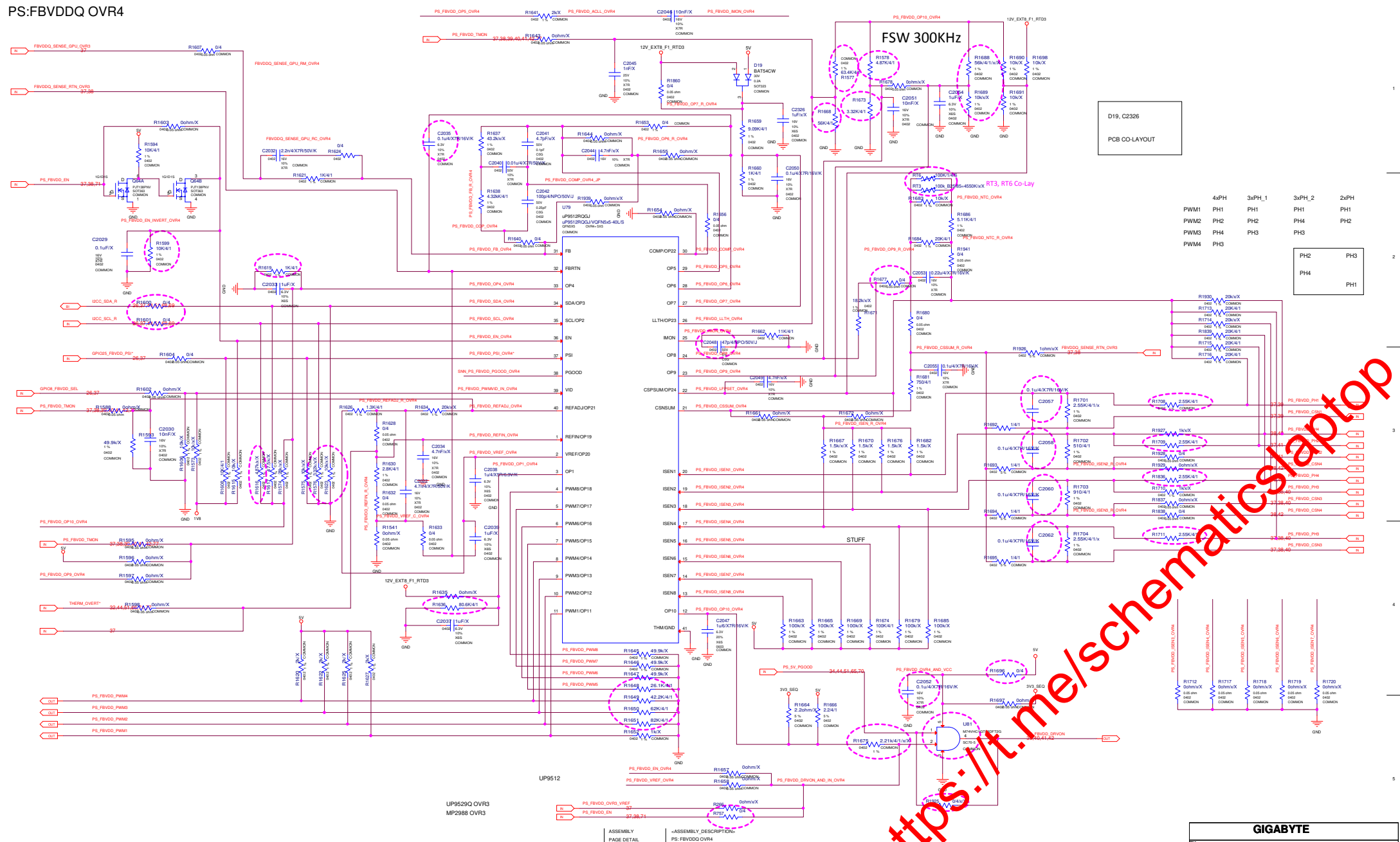
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Title			
PCIE			
Size	Document Number		Rev
Custom	GV-N3090EAGLE-24GD		1.0
Date:	Wednesday, September 30, 2020	Sheet	36 of 82

FOR DT SKU



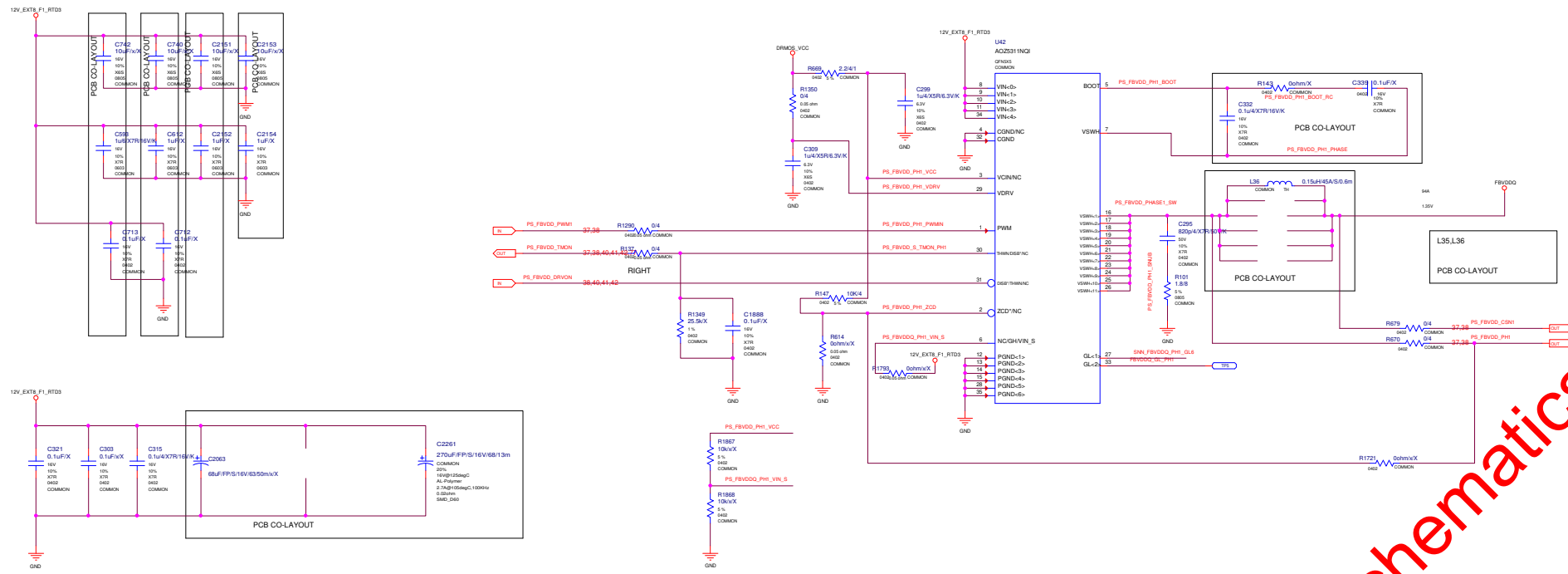
GIGABYTE			
Title			
PCIE			
Size	Document Number	Rev	
Custom	GV-N3090EAGLE-24GD	1.0	
Date:	Wednesday, September 30, 2020	Sheet	37 of 82

PS:FBVDDQ OVR4



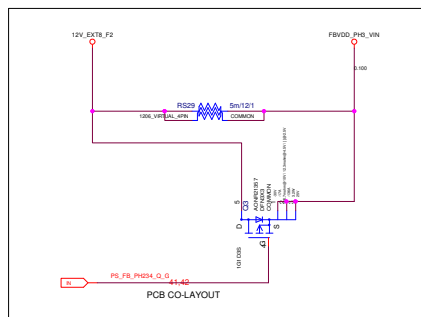
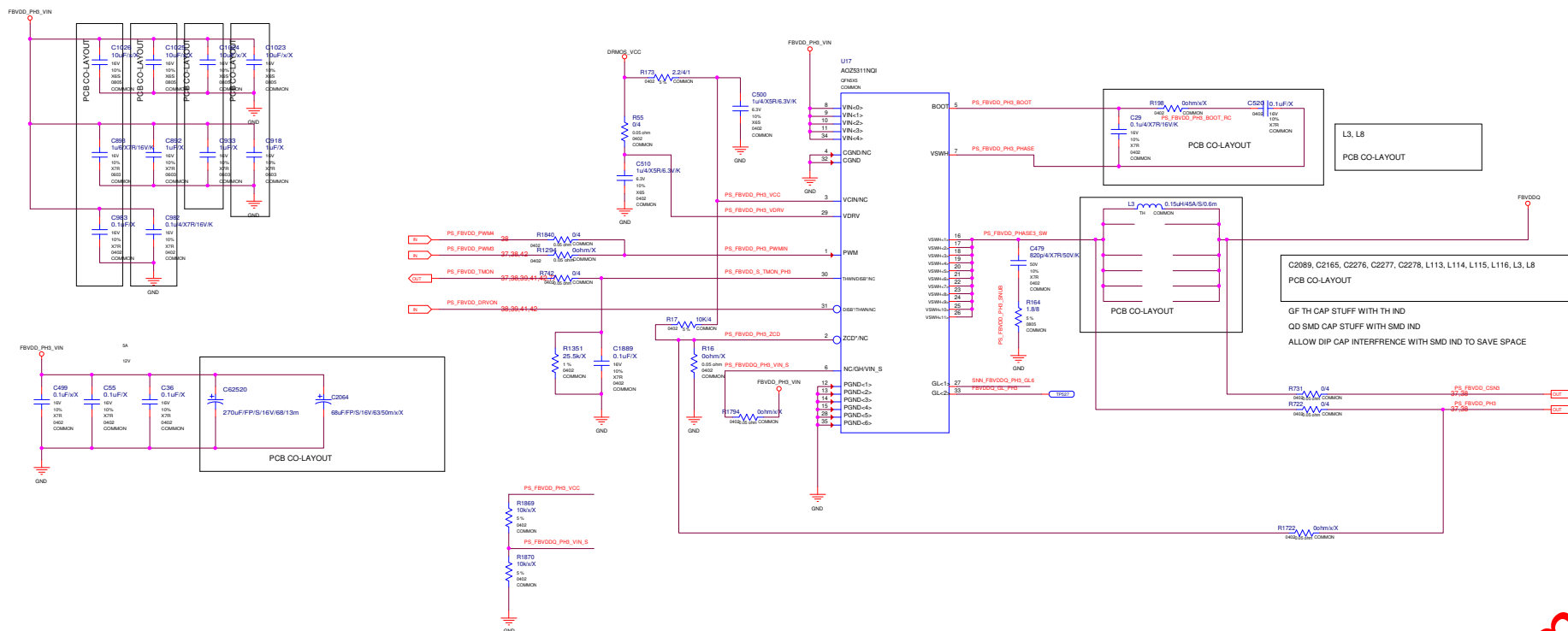
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PS: FBVDD PH1



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PS: FBVDD PH3



FBVDD PH3 INPUT ANTI-BACKDRIVE

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ASSEMBLY	<ASSEMBLY_DESCRIPTION>
PAGE DETAIL	PS: FBVDD PH3

The diagram illustrates the electrical connections for the PS_FBIW0_C0NS and PS_FBIW0_PHS components. It features a network of resistors and capacitors connected to a common ground. The components are labeled with their values and tolerances:

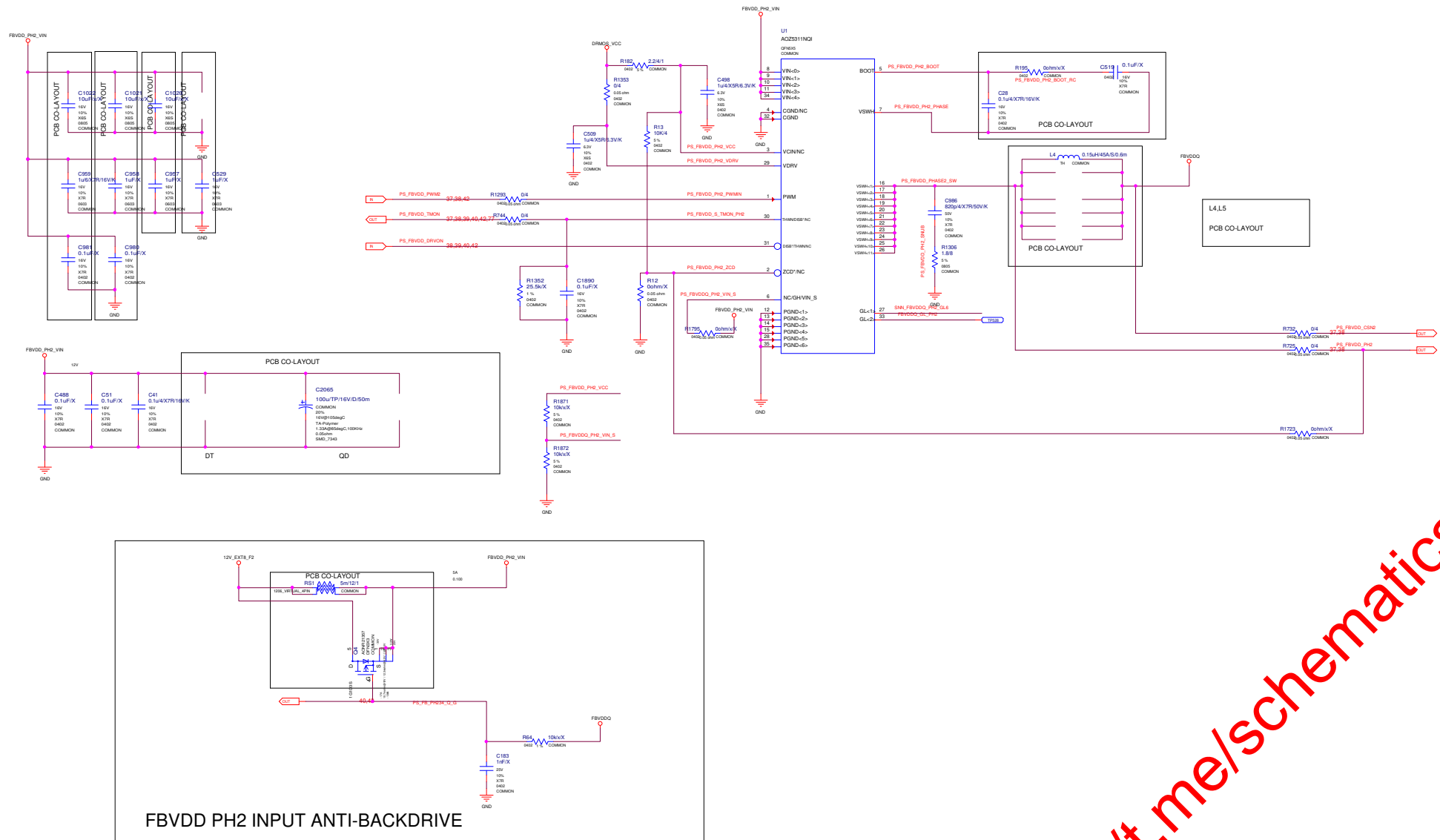
- R1731**: 100kΩ ±1%
- R1722**: 100kΩ ±1%
- R1723**: 100kΩ ±1%
- C1840**: 100pF ±1%
- C1841**: 100pF ±1%

The diagram is part of a larger document titled "GIGABYTE" and "PCIE".

GIGABYTE

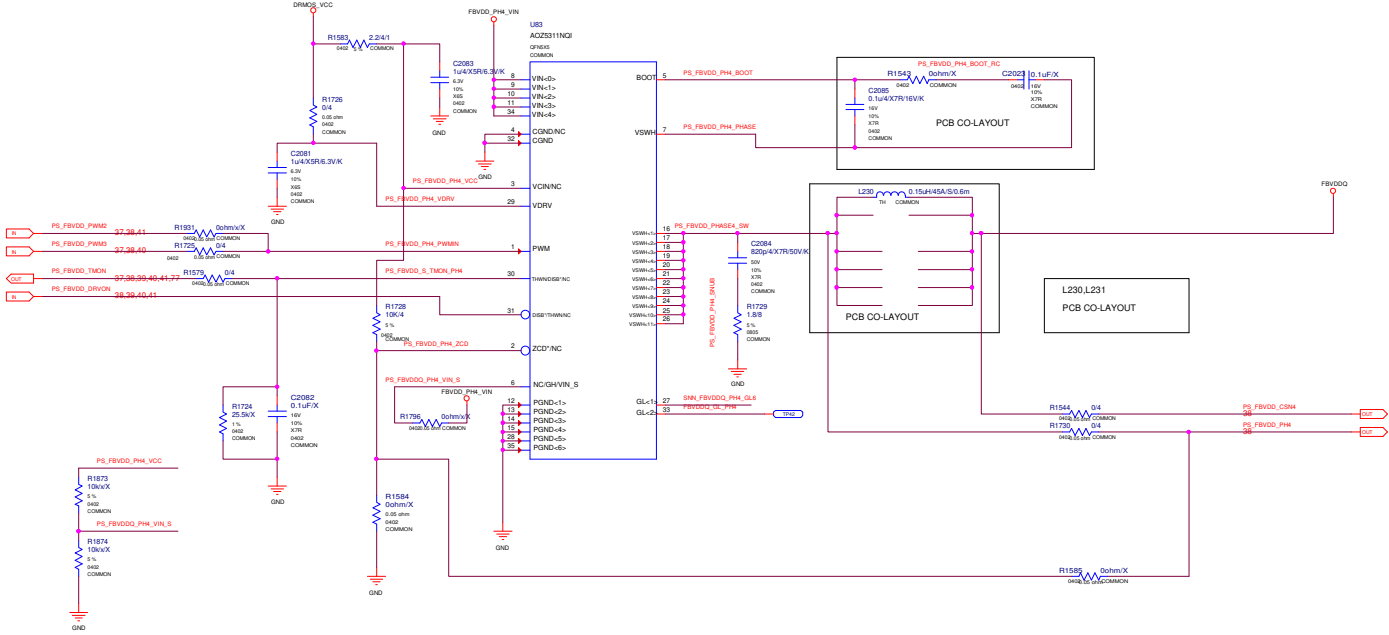
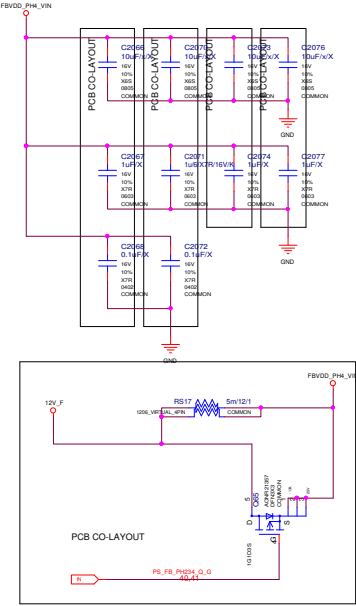
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PCIE			
Size	Document Number	Rev	
Custom	GV-N3090EAGLE-24GD	1.0	
Date:	Wednesday, September 30, 2020	Sheet	40 of 82

PS: FBVDD PH2

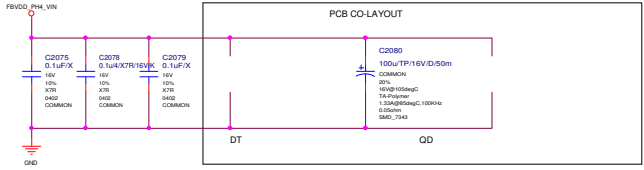


FBVDD PH2 INPUT ANTI-BACKDRIVE

PS: FBVDD PH4

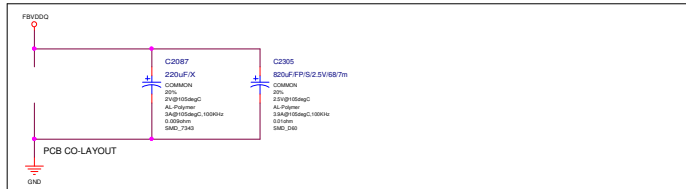


FBVDD PH3 INPUT ANTI-BACKDRIVE

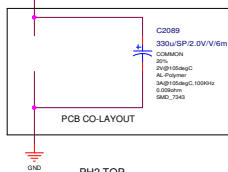


REFER TO PCB FOR BOM STUFFING

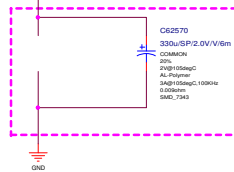
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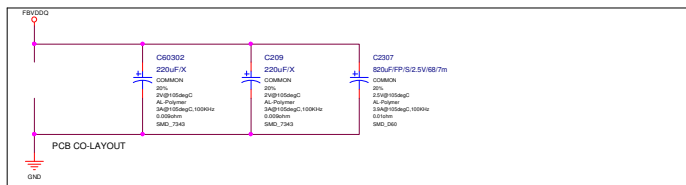
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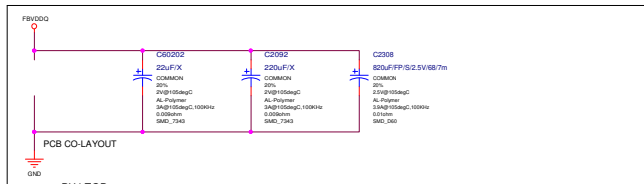
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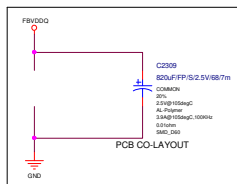
PH2 TOP



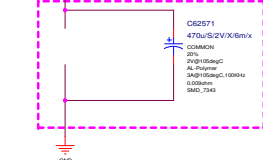
PH4 TOP



PH4 TOP

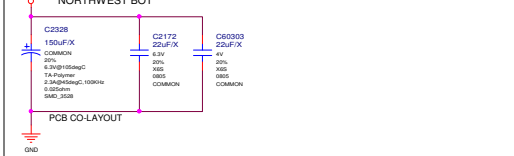


PH4 Bottom

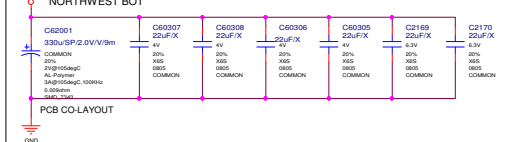


CLOSE TO FB PH3 OUTPUT-STUFF

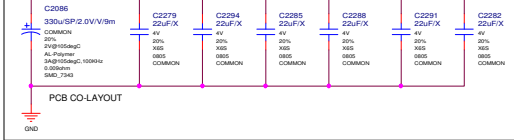
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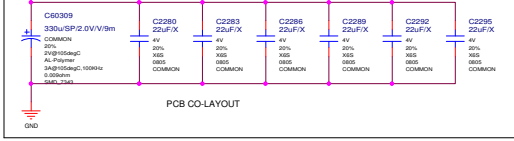
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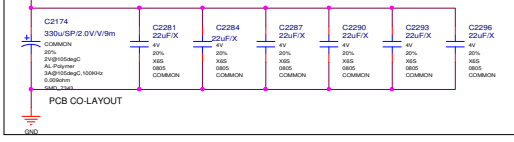
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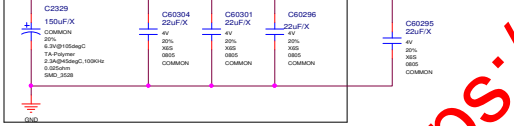
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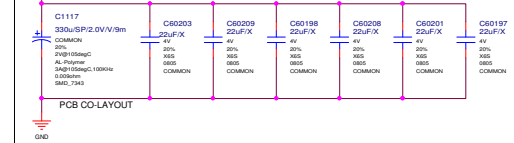
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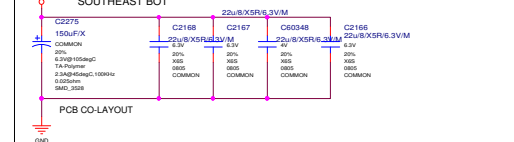
PCB CO-LAYOUT



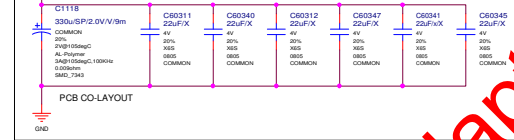
SOUTHEAST BOT



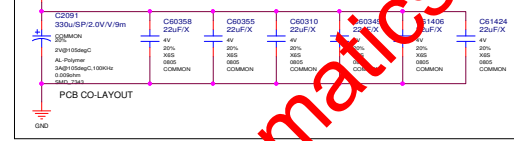
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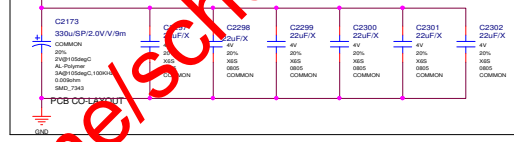
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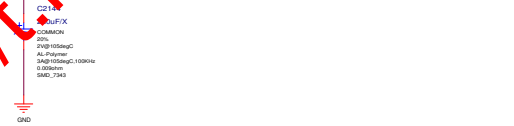
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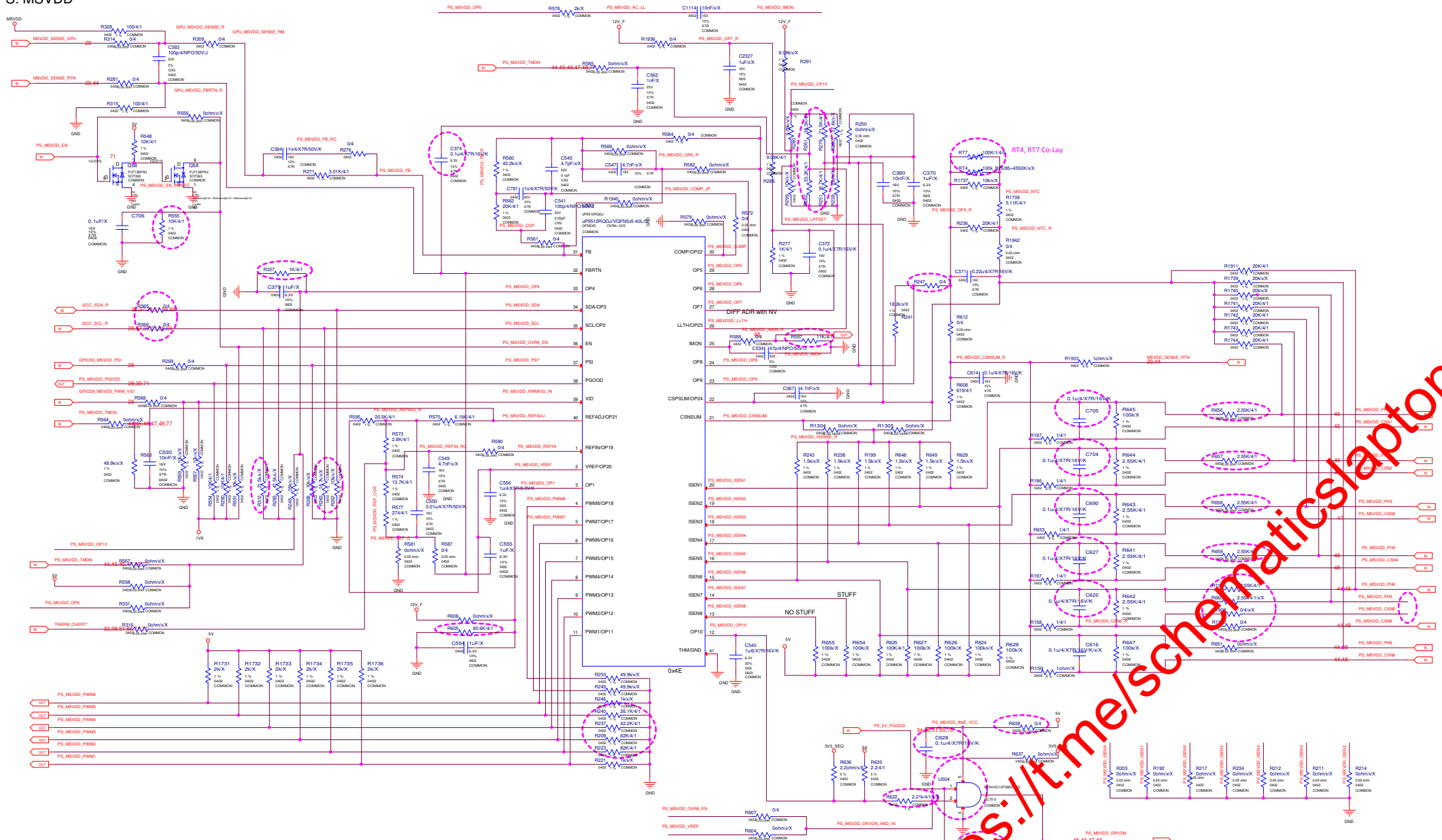
NORTHEAST TOP



PCB CO-LAYOUT



PS: MSVDD

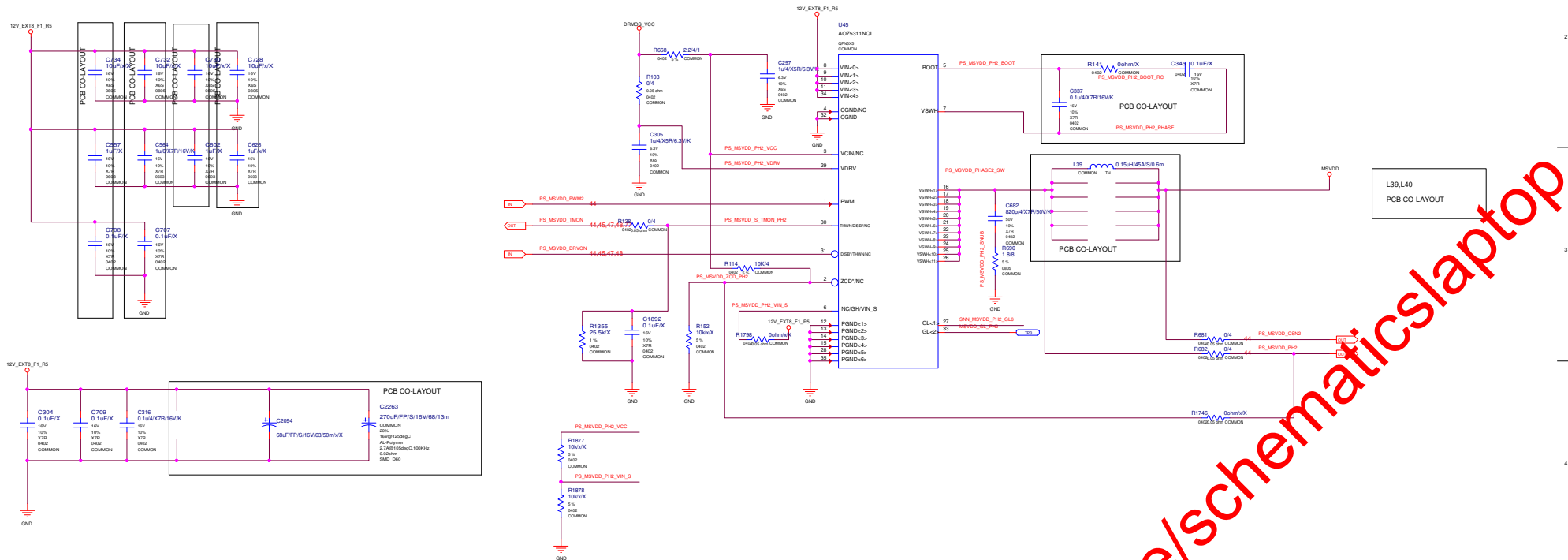


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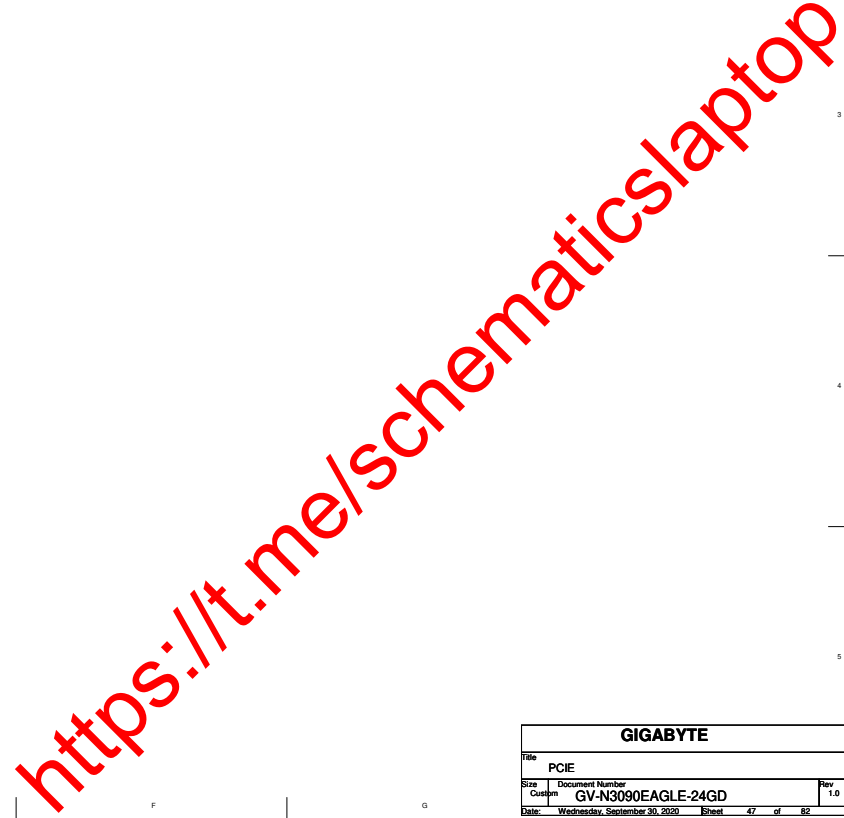
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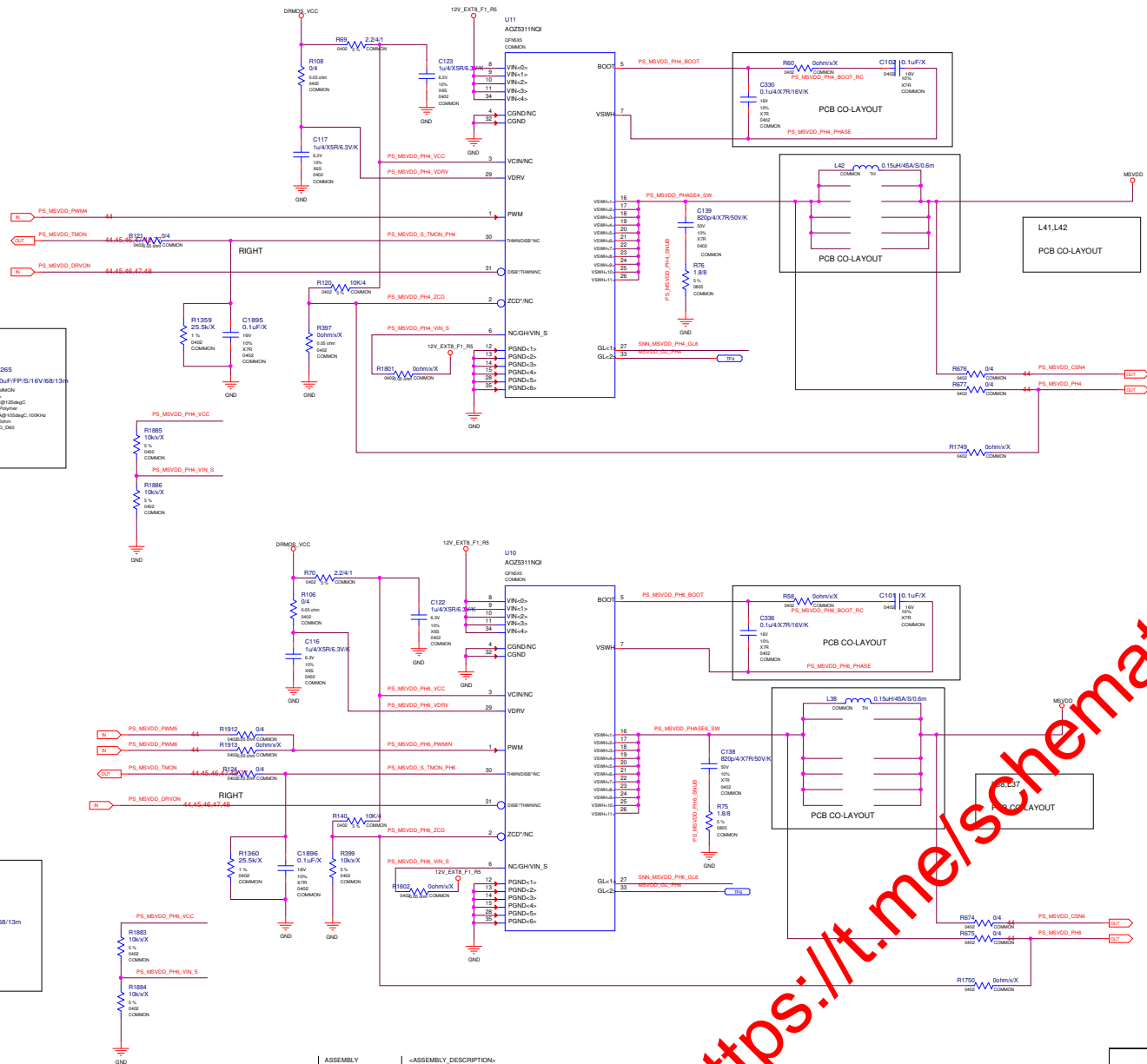
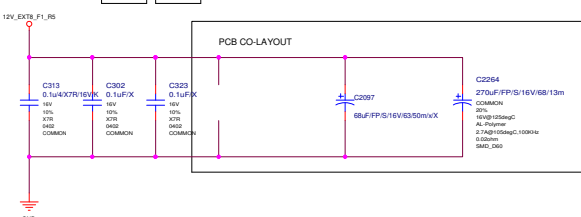
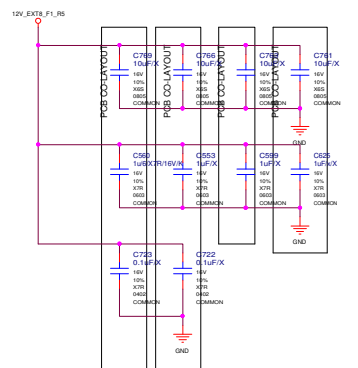
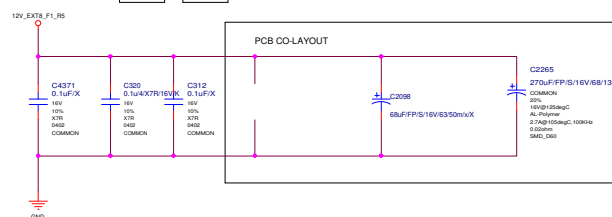
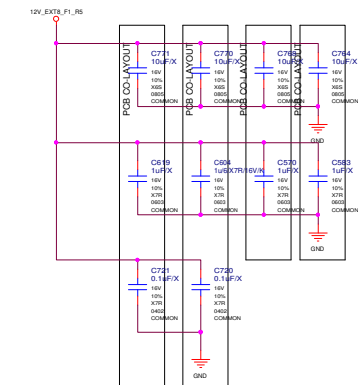
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Title PCIE				
Size	Document Number			Rev
Custom	GV-N3090EAGLE-24GD			1.0
Date:	Wednesday, September 30, 2020	Sheet	44	of 82

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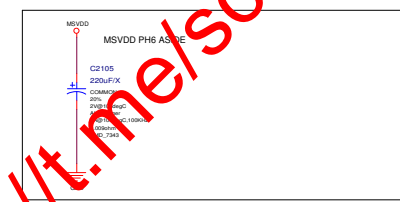
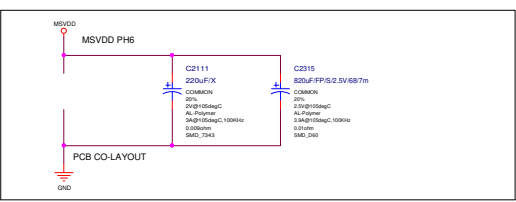
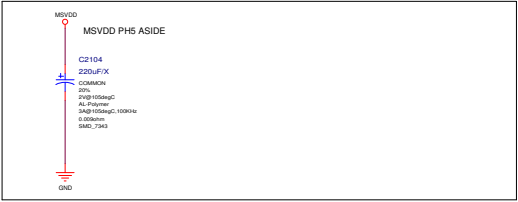
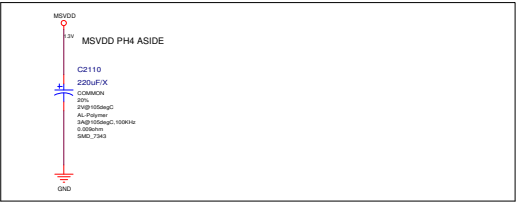
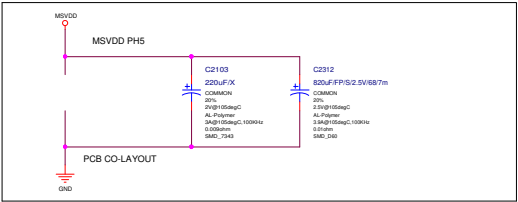
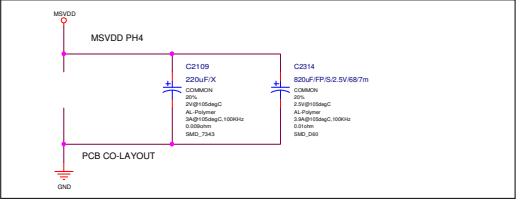
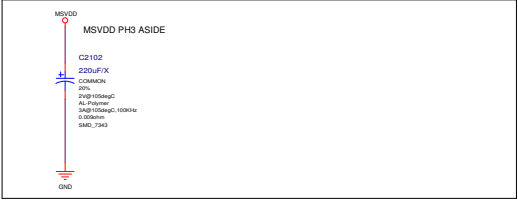
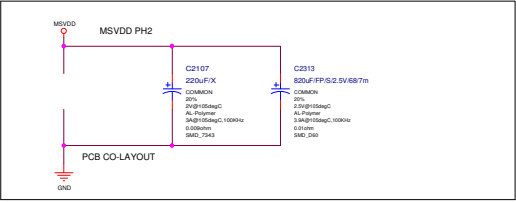
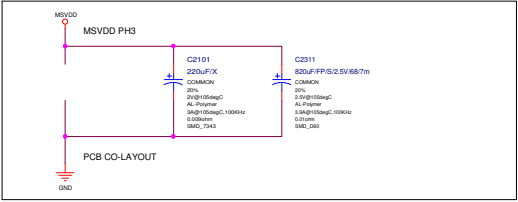
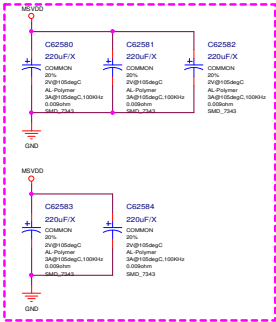
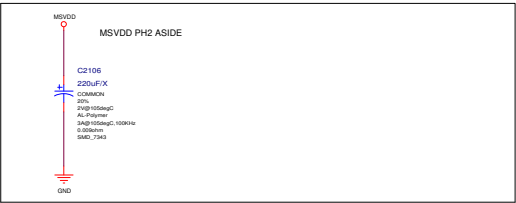
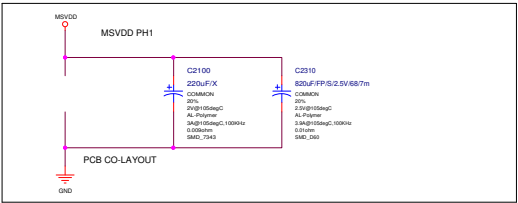


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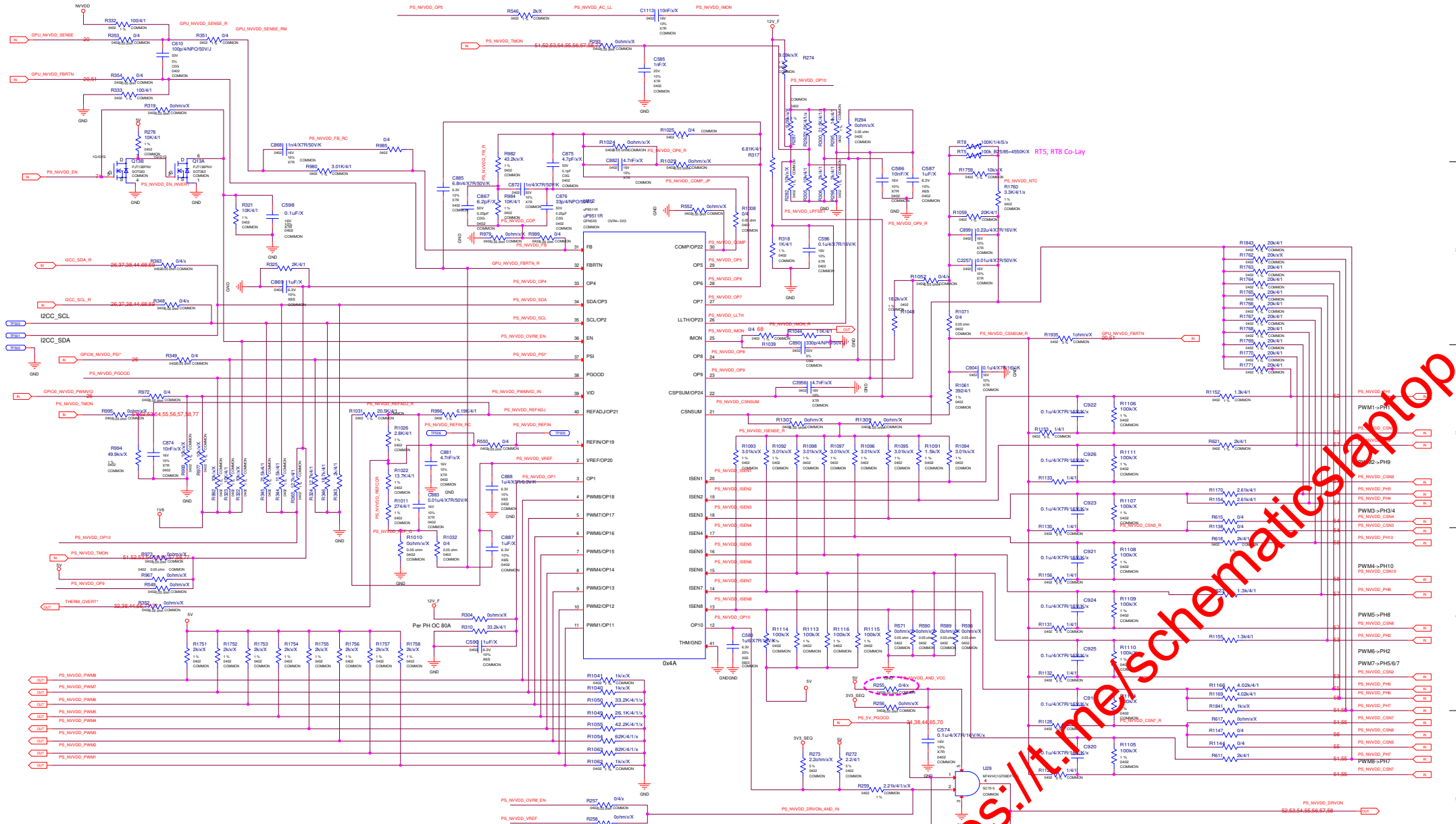
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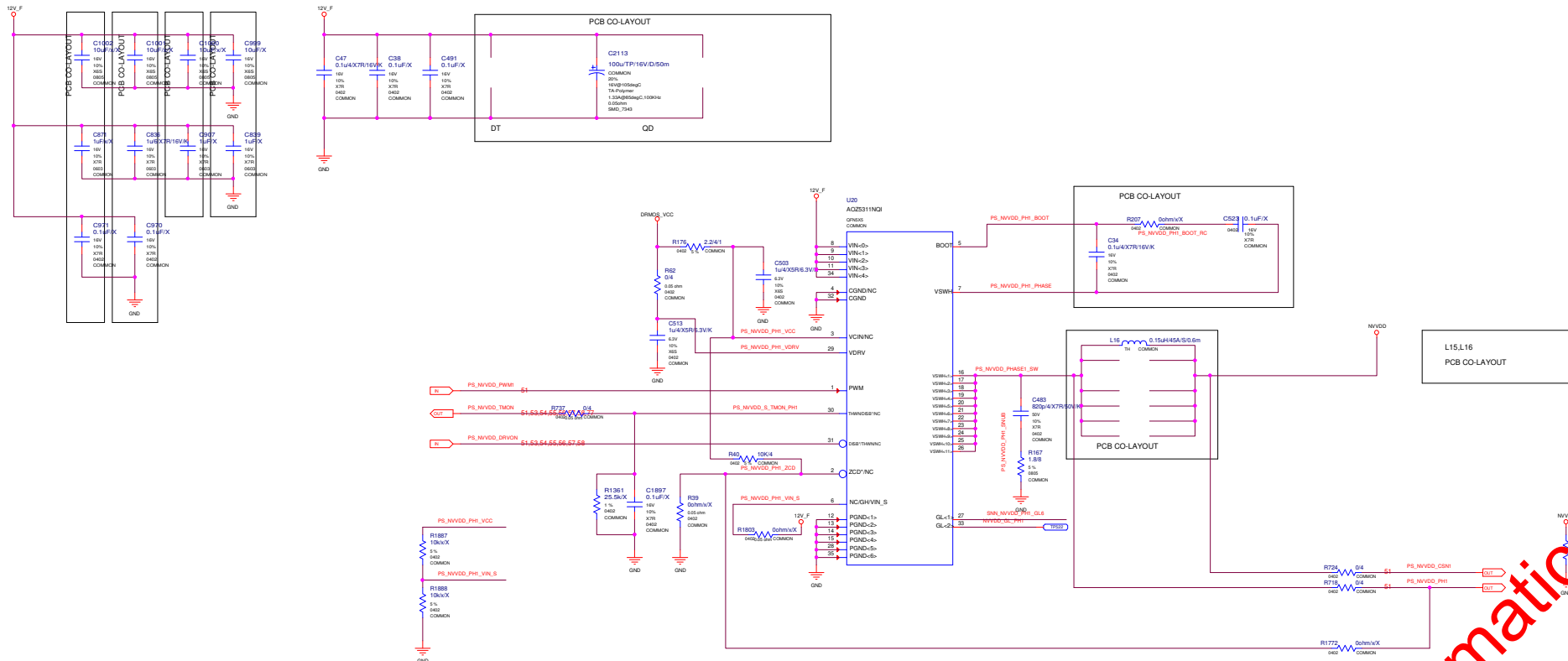
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PCIe			
Size	Document Number		Rev
Custom	GV-N3090EAGLE-24GD		1.0
Date:	Wednesday, September 30, 2020	Sheet	50 of 82

PS: NVVDD Controller_OVR8



PS: NVVDD Phase 1



PCB CO-LAYOUT

P5_MVDDO_CSMP

P5_MVDDO_PMT

P5_MVDDO

P5_MVDDO

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R1726 0402 100kOhm

R1727 0402 100kOhm

C1725 0402 100nF

C1726 0402 100nF

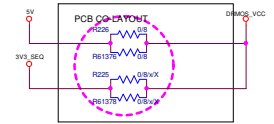
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D1725 0402 1N4148

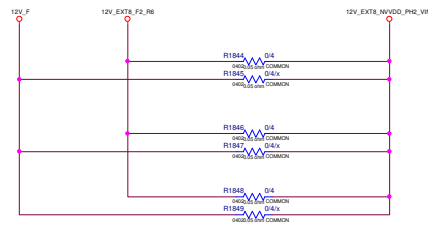
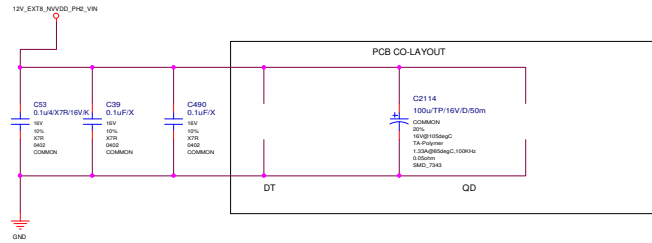
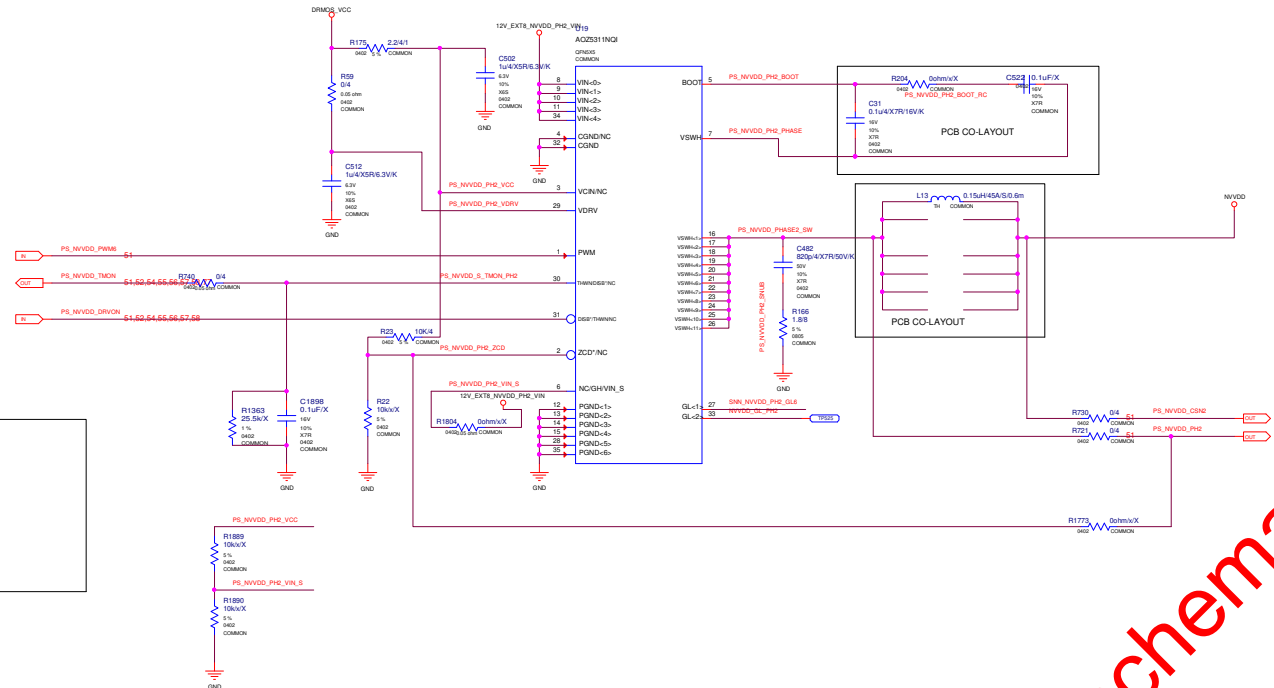
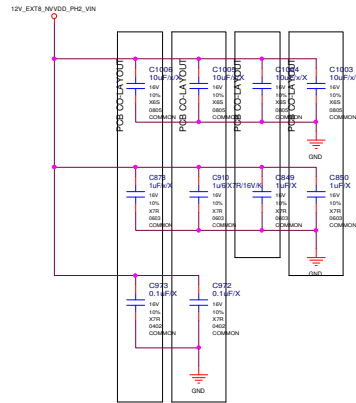
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GIGABYTE			
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Size	Document Number		
Custom	GV-N3090EAGLE-24GD		
Date:	Wednesday, September 30, 2020	Sheet	50 of 50

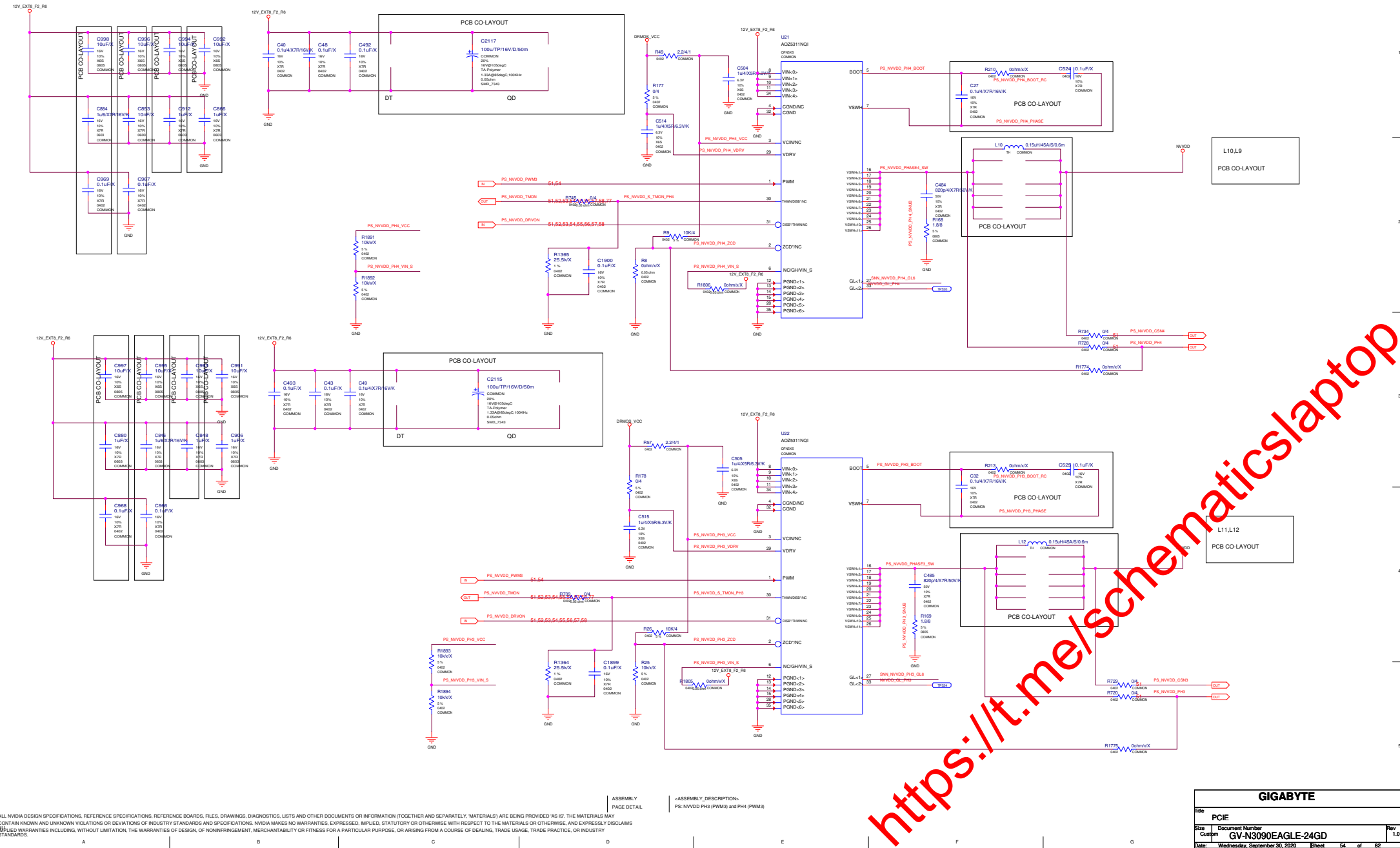
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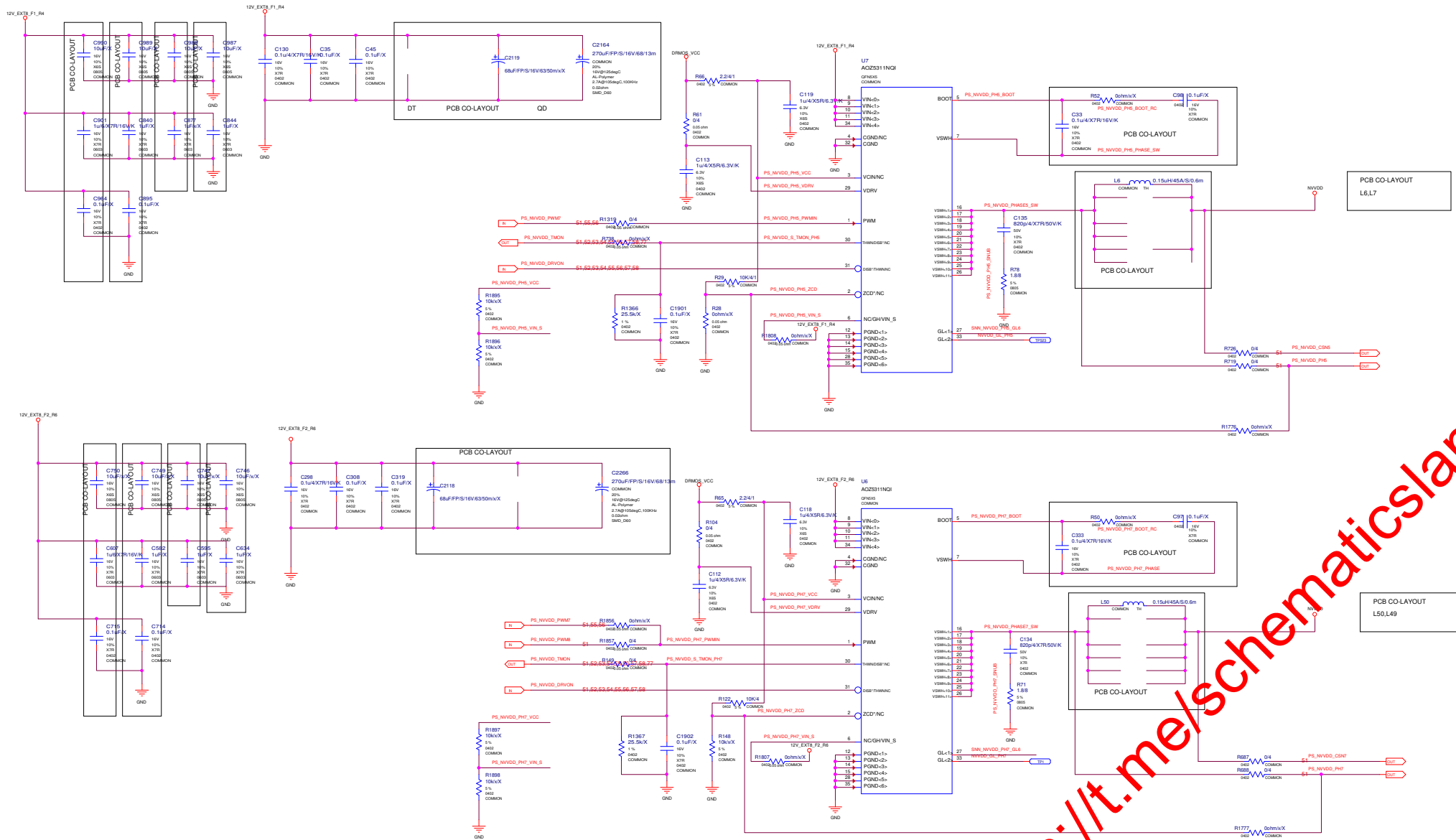
MPS DRMOS WITH 3V3 SUPPLY



PS: NVVDD Phase 3 (PWM3), PHASE4 (PWM3)



PS: NVVDD PH 5(PWM4), PH7(PWM8/4)



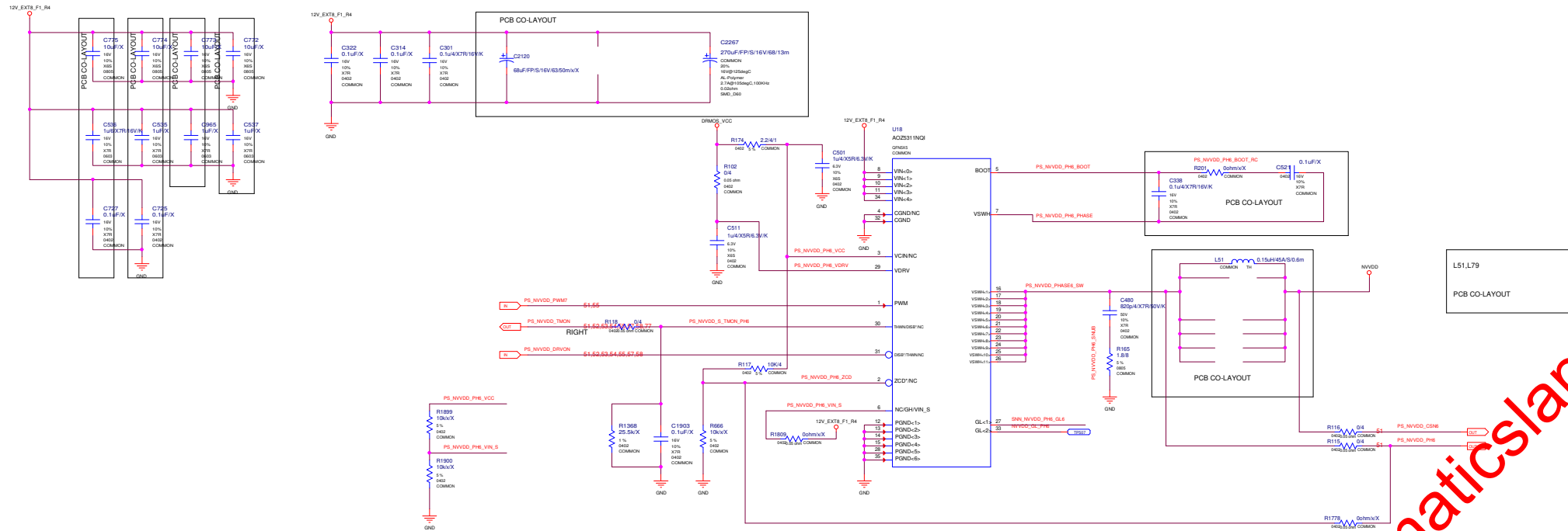
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ASSEMBLY	<ASSEMBLY_DESCRIPTION>
PAGE DETAIL	PS: NVDD PH5 (PWM7) and PH7 (PWM8)

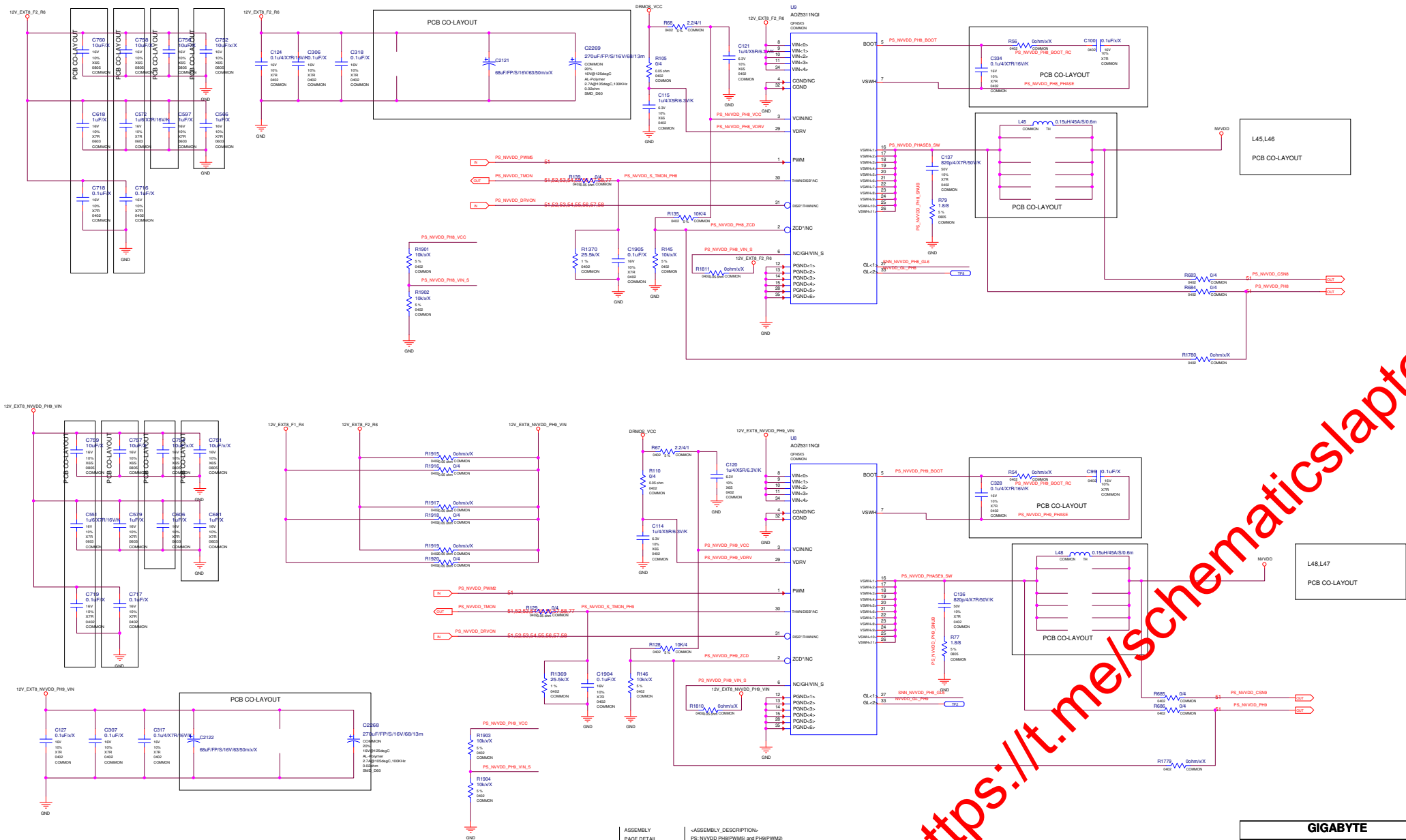
GIGABYTE

GIGABYTE			
Title: PCIE			
Size	Document Number	Rev	
Custom	GV-N3090EAGLE-24GD	1.0	
Date:	Wednesday, September 30, 2020	Sheet	55 of 82

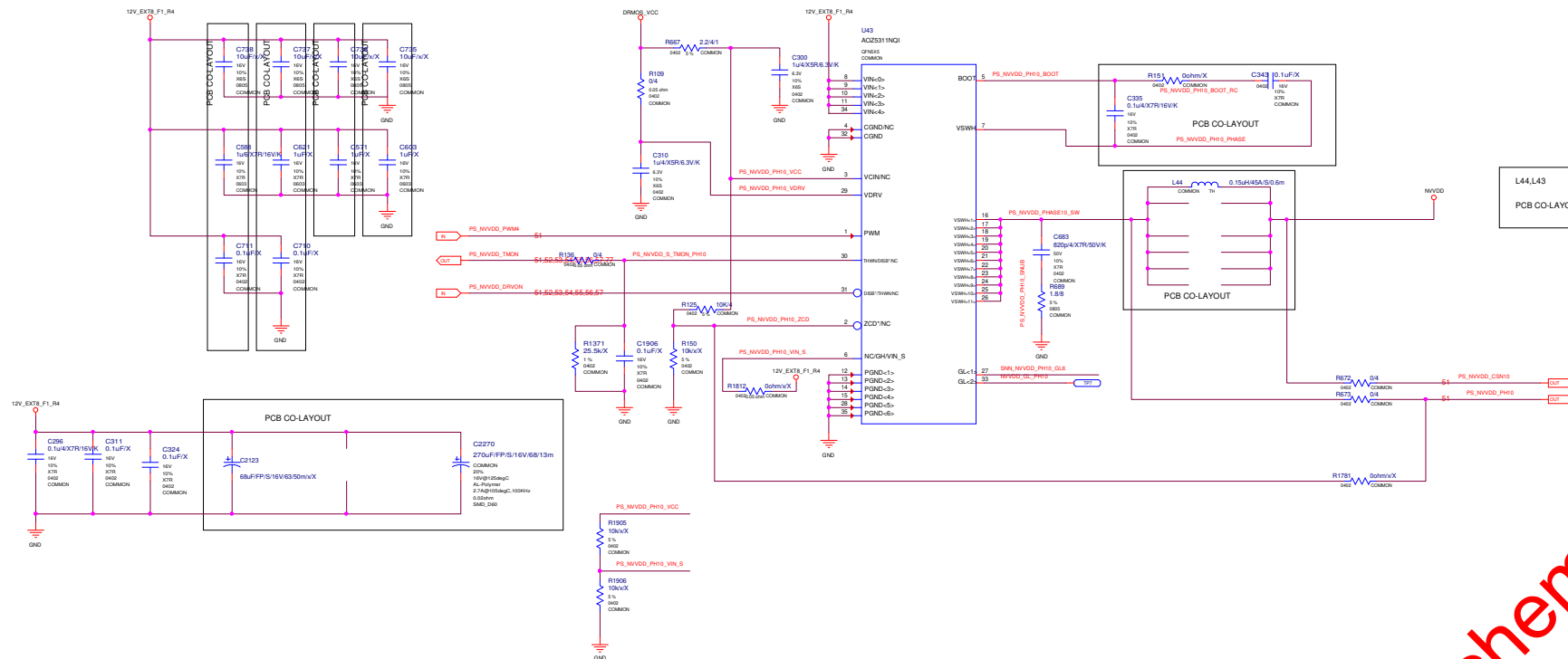
PS: NVVDD Phase 6(PWM4)



PS: NVVDD PH 8 (PWM5), PH 9(PWM2)



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PCB CO-LAYOUT

R172 0402 100kOhm

R173 0402 100kOhm

R174 0402 100kOhm

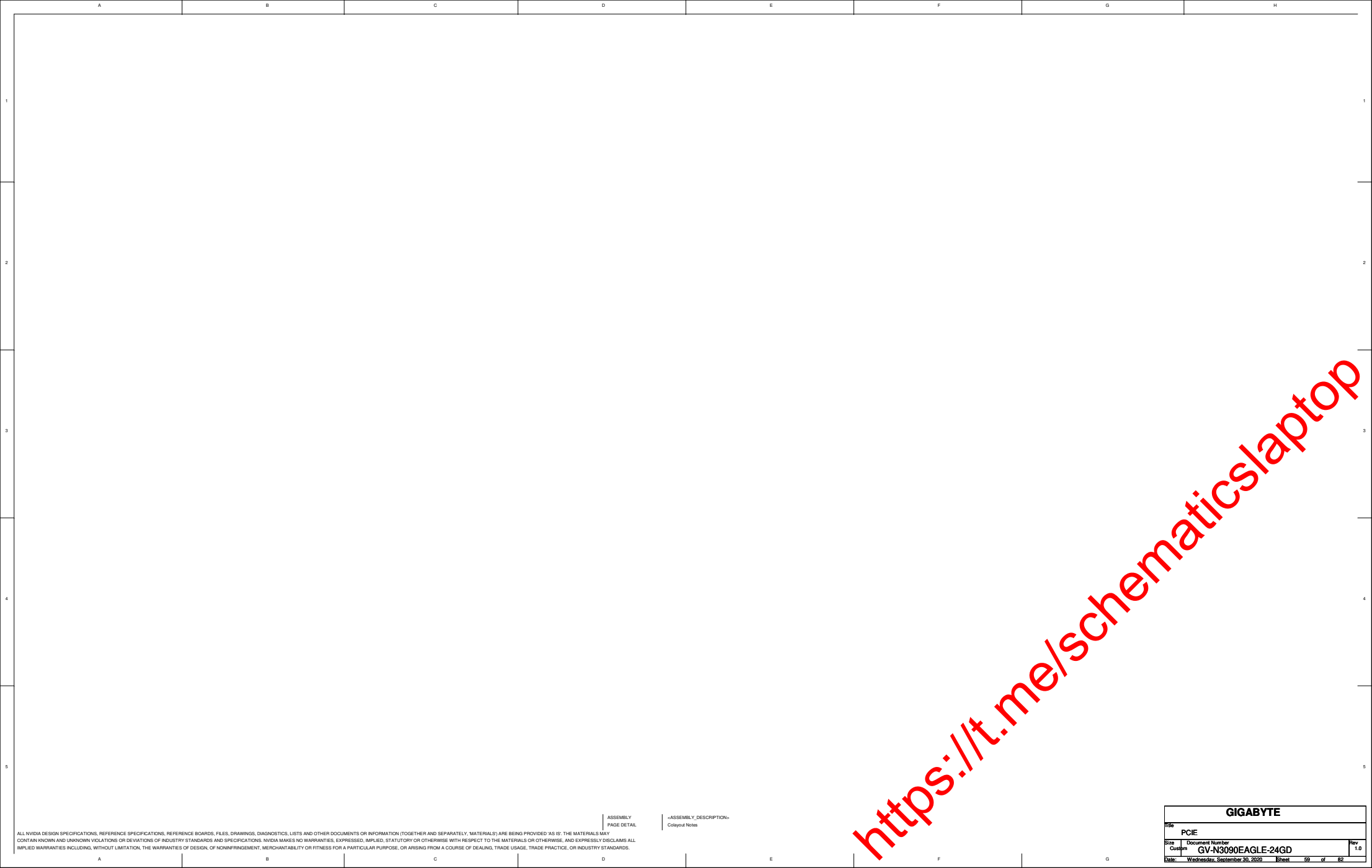
R175 0402 100kOhm

PS_NWDD_CSINIS

PS_NWDD_PHS

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GIGABYTE			
Title			
PCIE			
Size	Document Number		Rev
Custom	GV-N3090EAGLE-24GD		1.0
Date:	Wednesday, September 30, 2020	Sheet	58 of 80



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PAGE DETAIL

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Size	Document Number		Rev
Custom	GV-N3090EAGLE-24GD		1.0
Date:	Wednesday, September 30, 2020	Sheet	59 of 82

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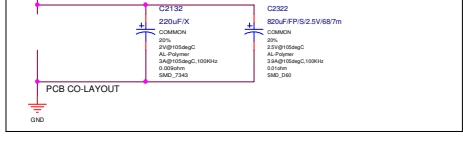
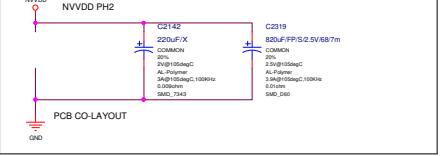
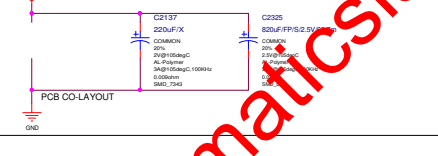
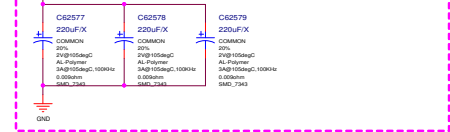
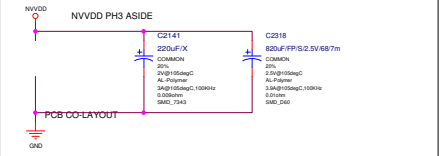
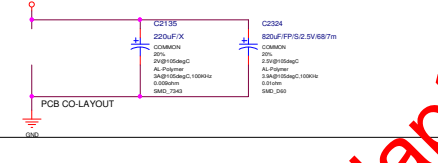
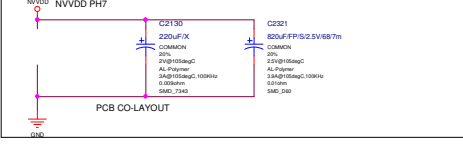
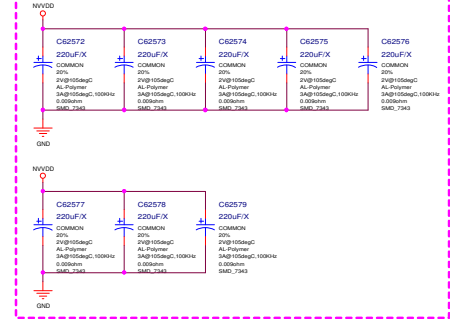
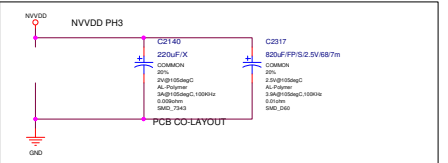
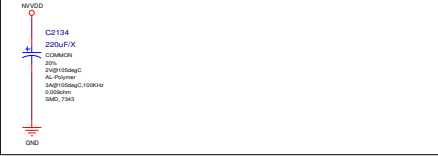
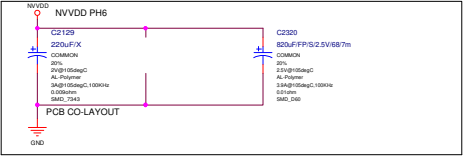
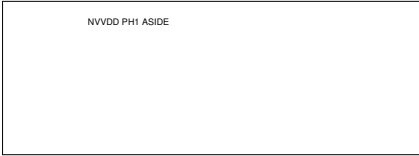
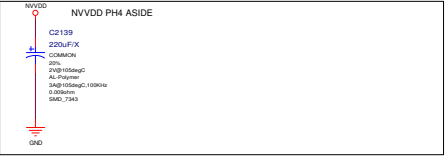
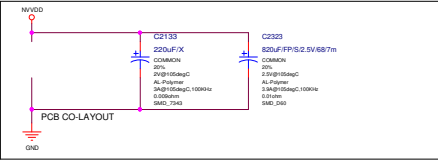
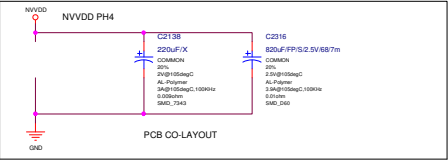
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GIGABYTE			
Title			
PCIe			
Size	Document Number		Rev
Custom	GV-N3090EAGLE-24GD		1.0
Date:	Wednesday, September 30, 2020	Sheet	60 of 82

REFER TO PCB FOR BOM STUFFING



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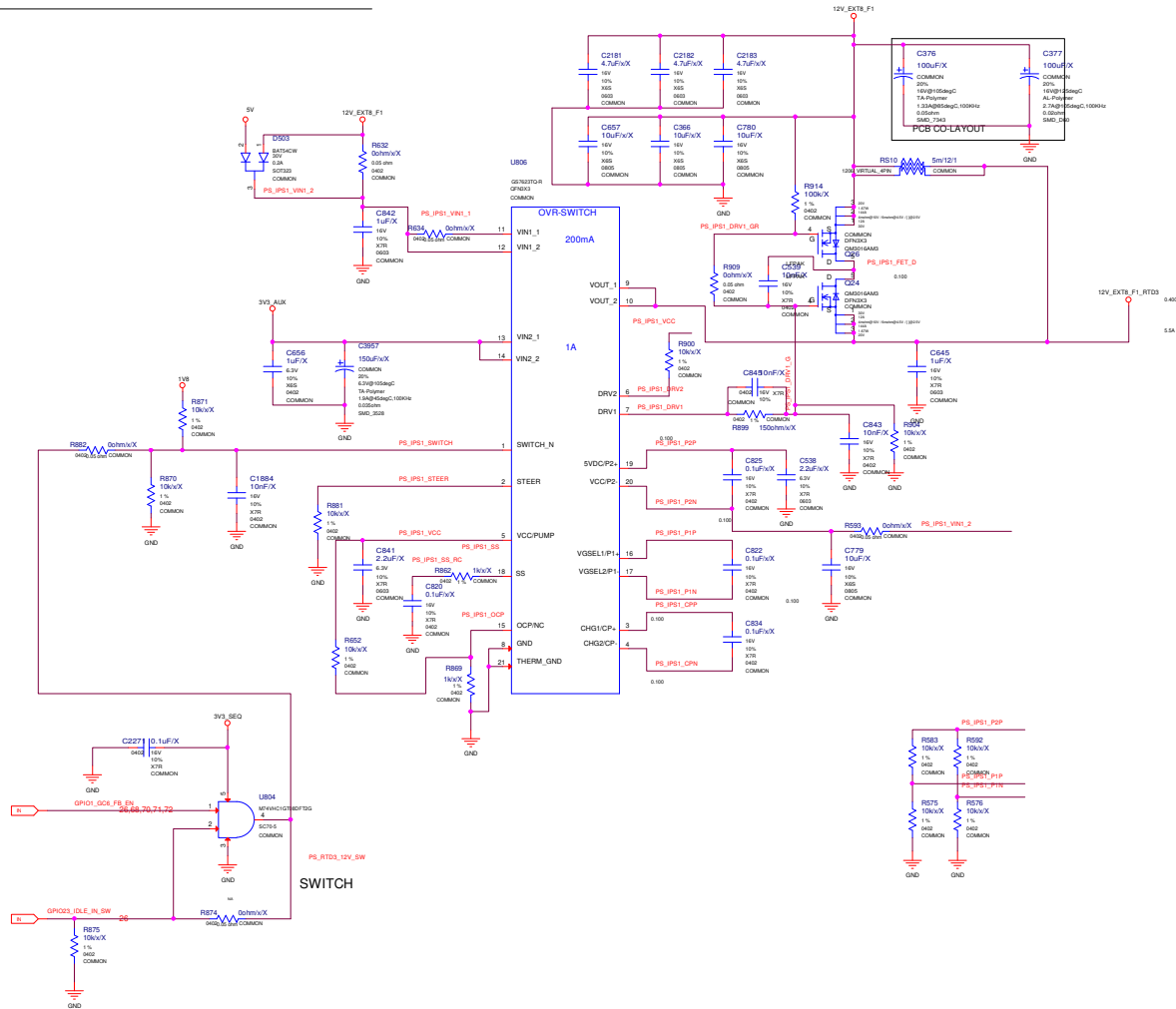
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GIGABYTE			
Title			
PCI-E			
Size	Document Number		Rev
Custom	GV-N3090EAGLE-24GD		1.0
Date:	Wednesday, September 30, 2020	Sheet	62 of 82

PS: INPUT SWITCH RTD3

AND GATE LOGIC FOR P-BOARD

GPIO1	GPIO23	SWITCH	VOUT
0	0	0	12V_F
0	1	0	12V_F
1	0	0	12V_F
1	1	1	3V3A



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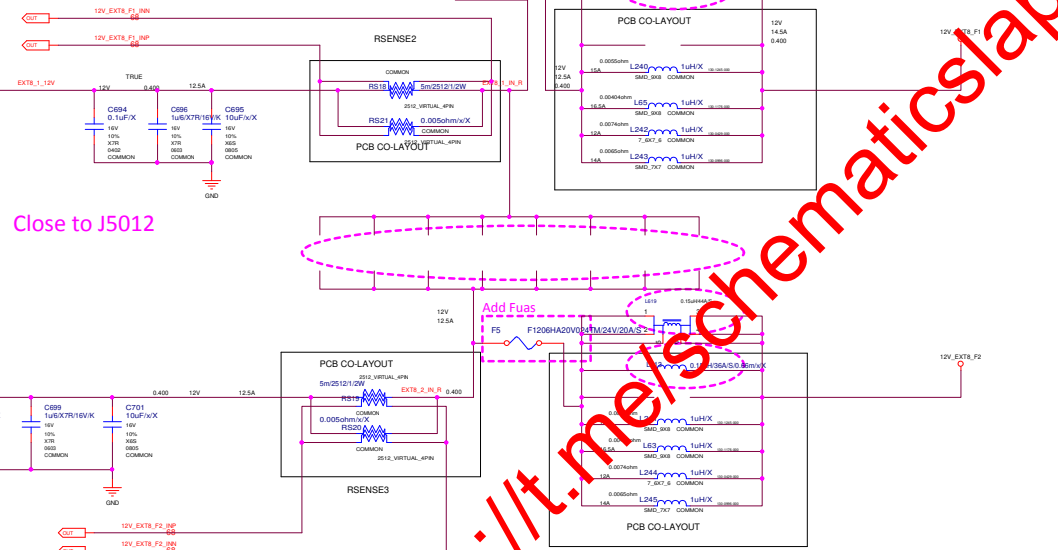
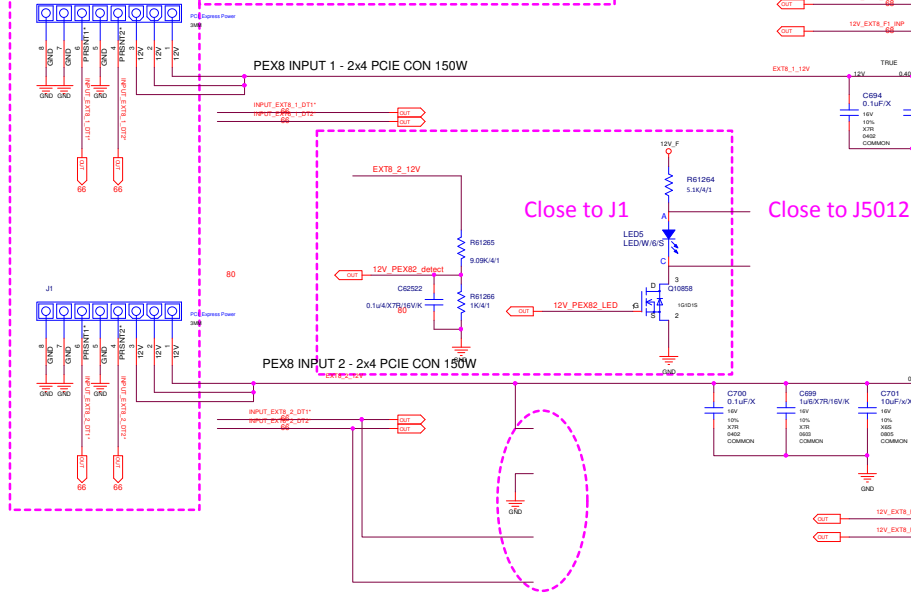
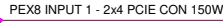
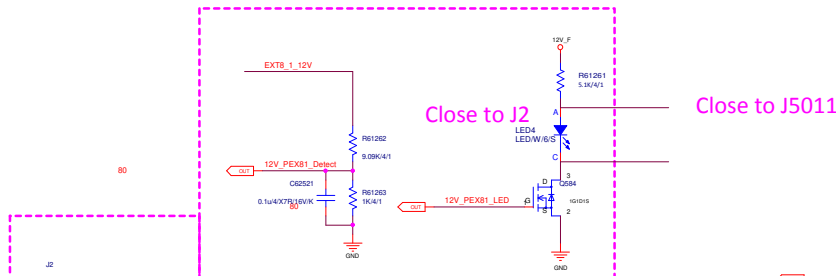
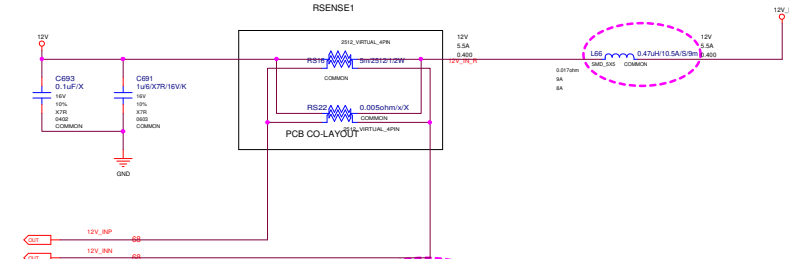
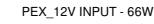
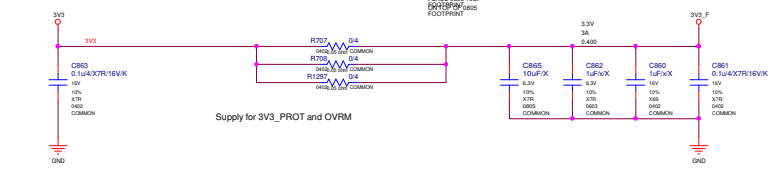
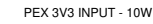
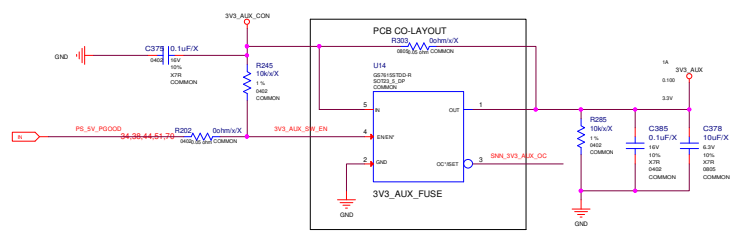
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GIGABYTE			
Title			
PCI-E			
Size	Document Number		Rev
Custom	GV-N3090EAGLE-24GD		1.0
Date:	Wednesday, September 30, 2020	Sheet	64 of 82

PS: Inputs, Filtering, and Monitoring



The schematic diagram illustrates the power supply system for the GIGABYTE GV-N3090EAGLE-24GD. It shows the connection between the power supply unit (PSU) and the motherboard (PCB CO-LAYOUT). The PSU is labeled F1206HA20V084TM and provides 12V, 14.5A, and 0.400A. The motherboard is labeled PCB CO-LAYOUT and includes components like RSENSE1, RSENSE2, RSENSE3, L243, L244, L245, and F4, F5. The diagram is labeled with 'Add Fuses' and 'PCB CO-LAYOUT'. A large red watermark 'https://t.me/schematics4laptop' is overlaid on the diagram.

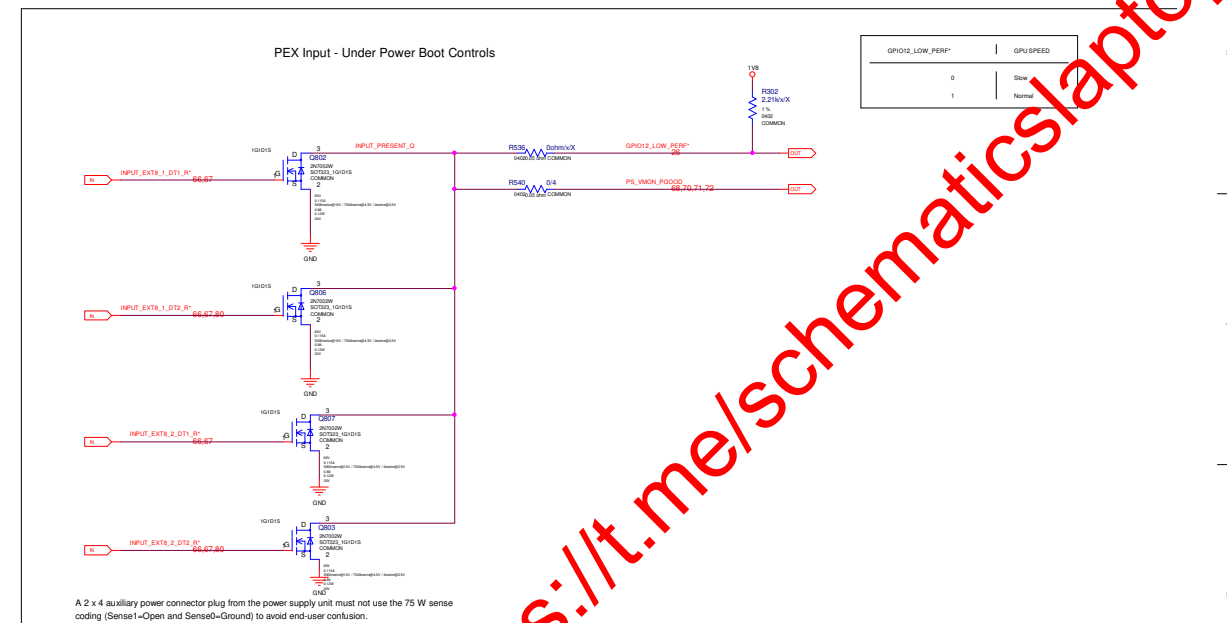
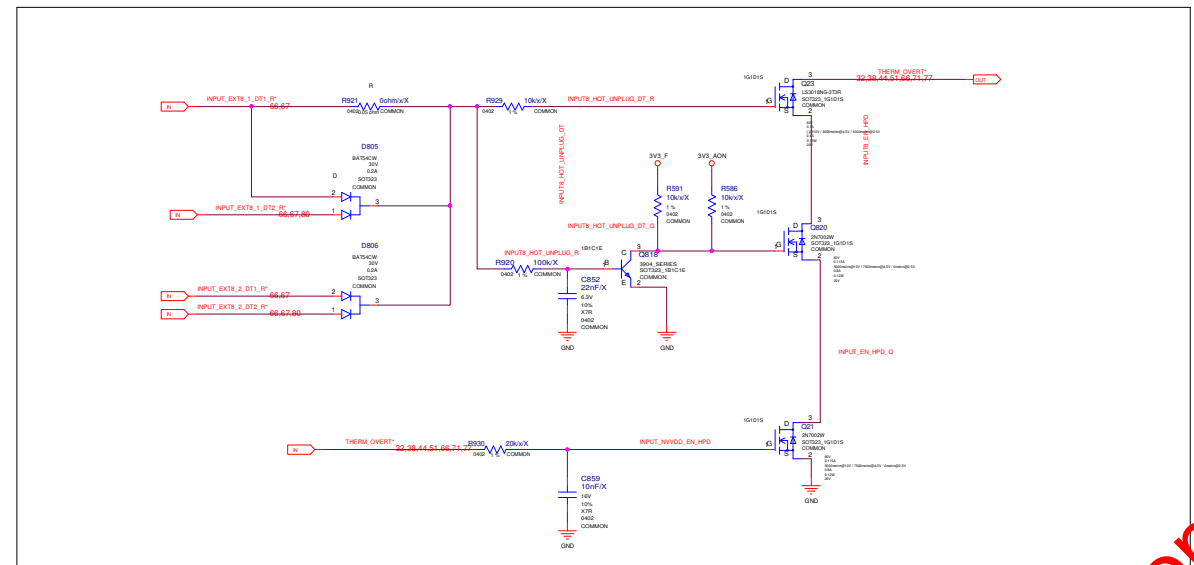
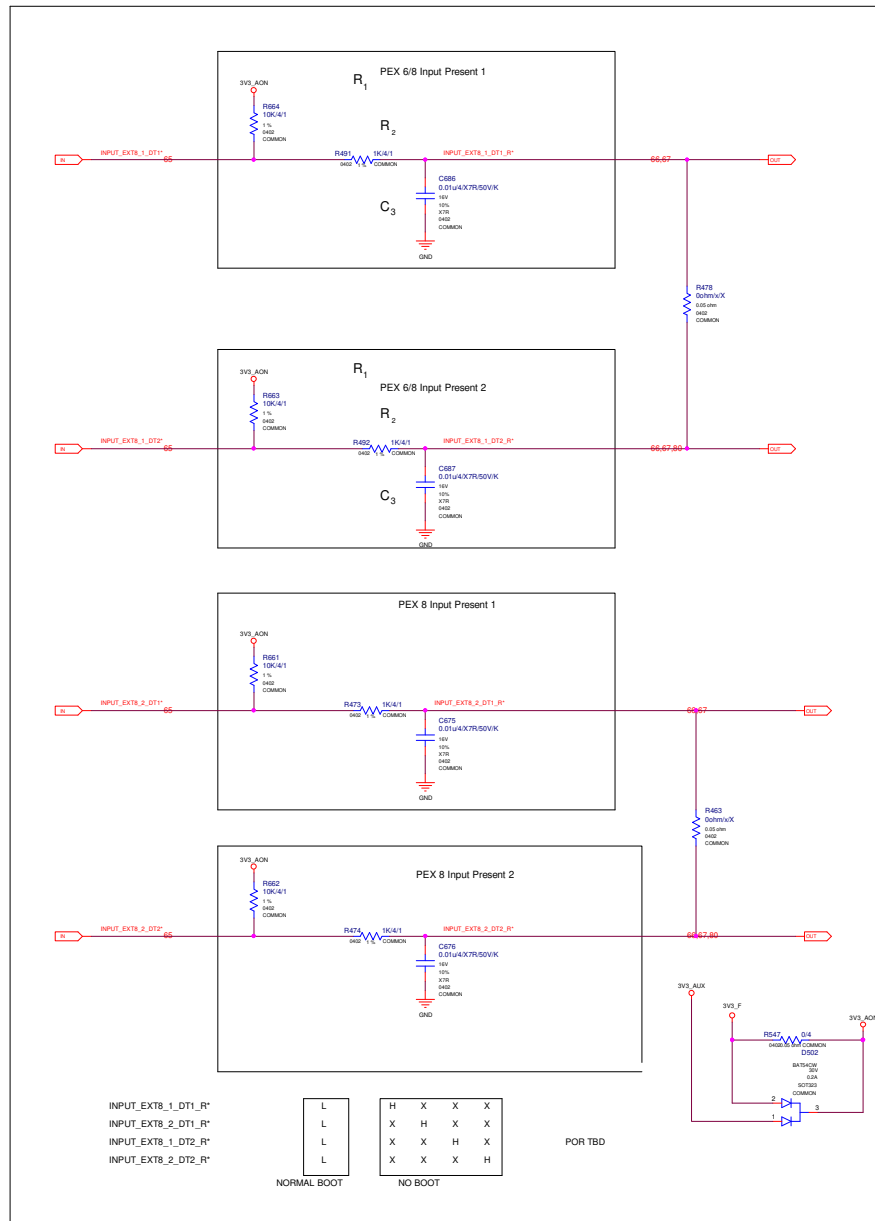
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ASSEMBLY	<ASSEMBLY_DESCRIPTION>
PAGE DETAIL	PS: INPUTS, FILTERING, and, MONITORING

GIGABYTE

Title			
PCIE			
Size	Document Number		Rev
Custom	GV-N3090EAGLE-24GD		1.0
Date:	Wednesday, September 30, 2020	Sheet	65 of 82

PS: 12V Current Steering & Hot Unplug Detect

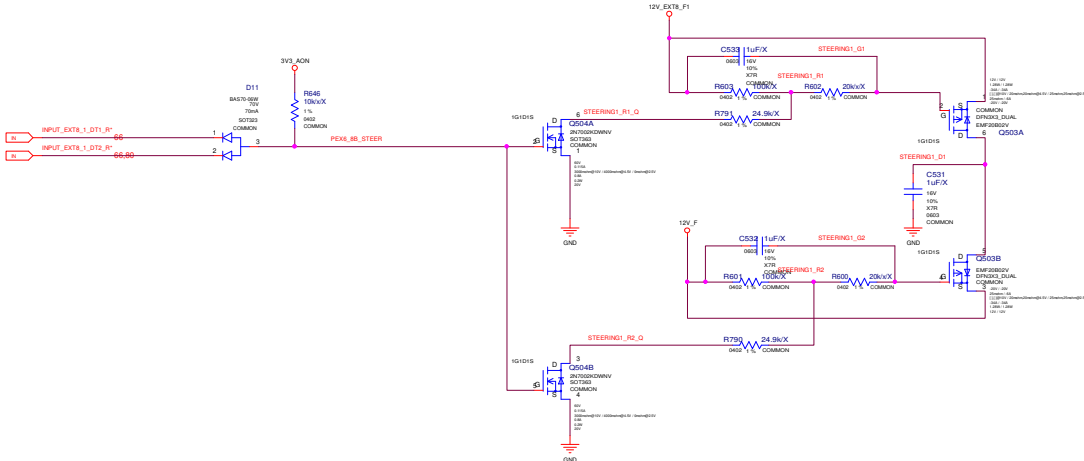


A 2 x 4 auxiliary power connector plug from the power supply unit must not use the 75 W sense coding (Sense1=Open and Sense0=Ground) to avoid end-user confusion.

PS: Discrete Steering

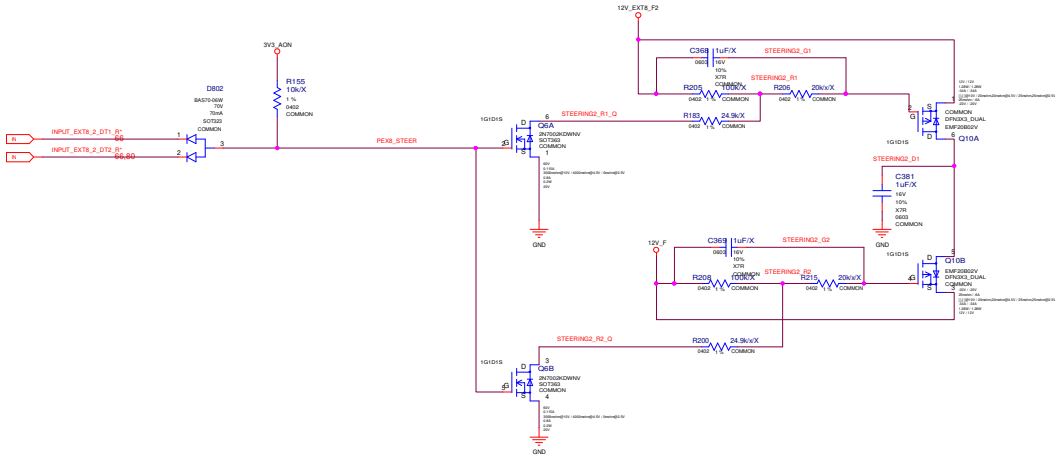
12V CURRENT STEERING (UNDER POWER BOOT):

PEX12V AND 12V_EXT8_F1



NO STUFF BY DEFAULT REF ONLY

PEX12V AND 12V_EXT8_F2

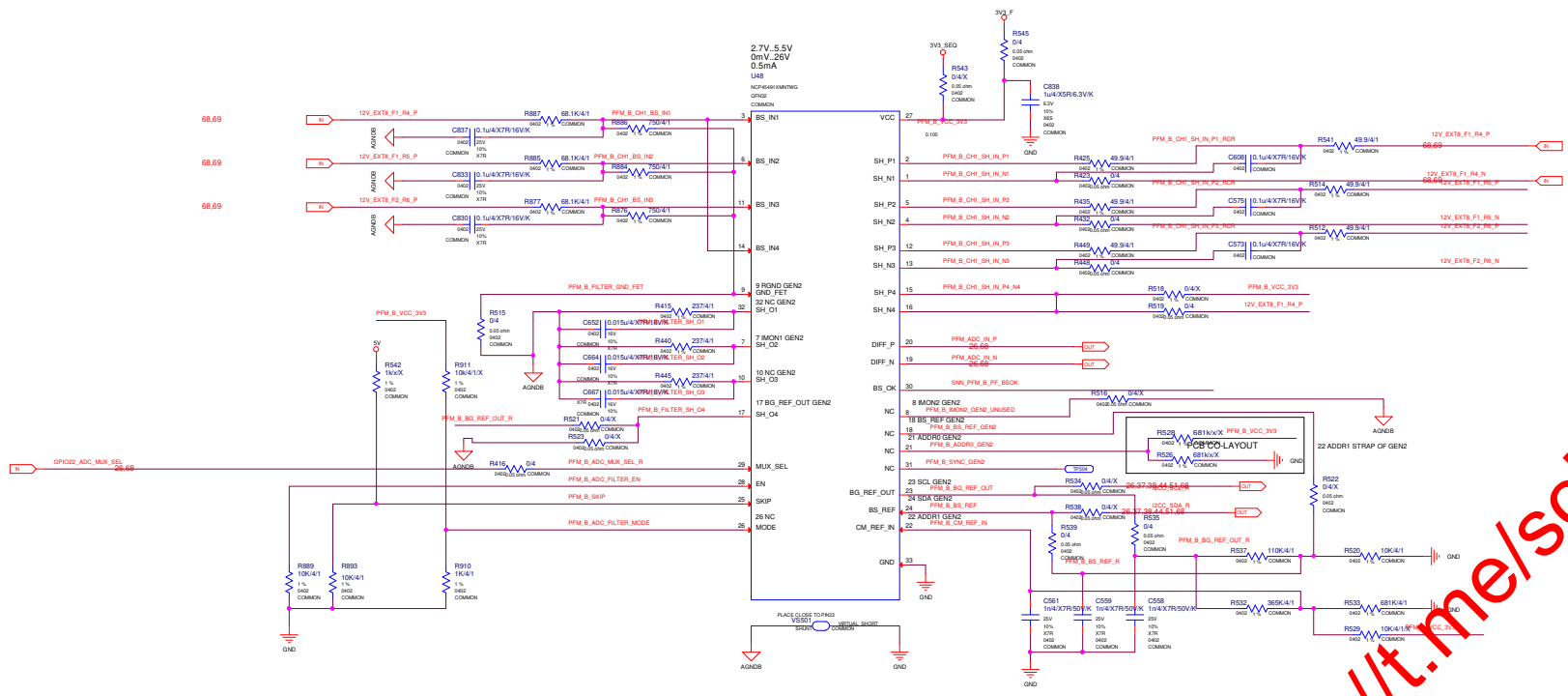
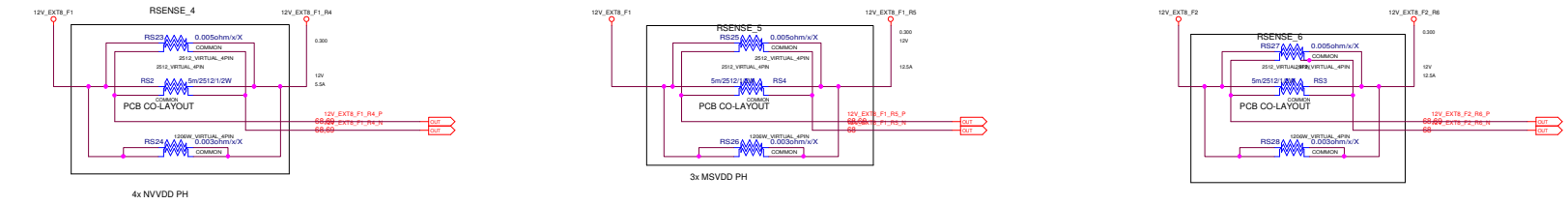


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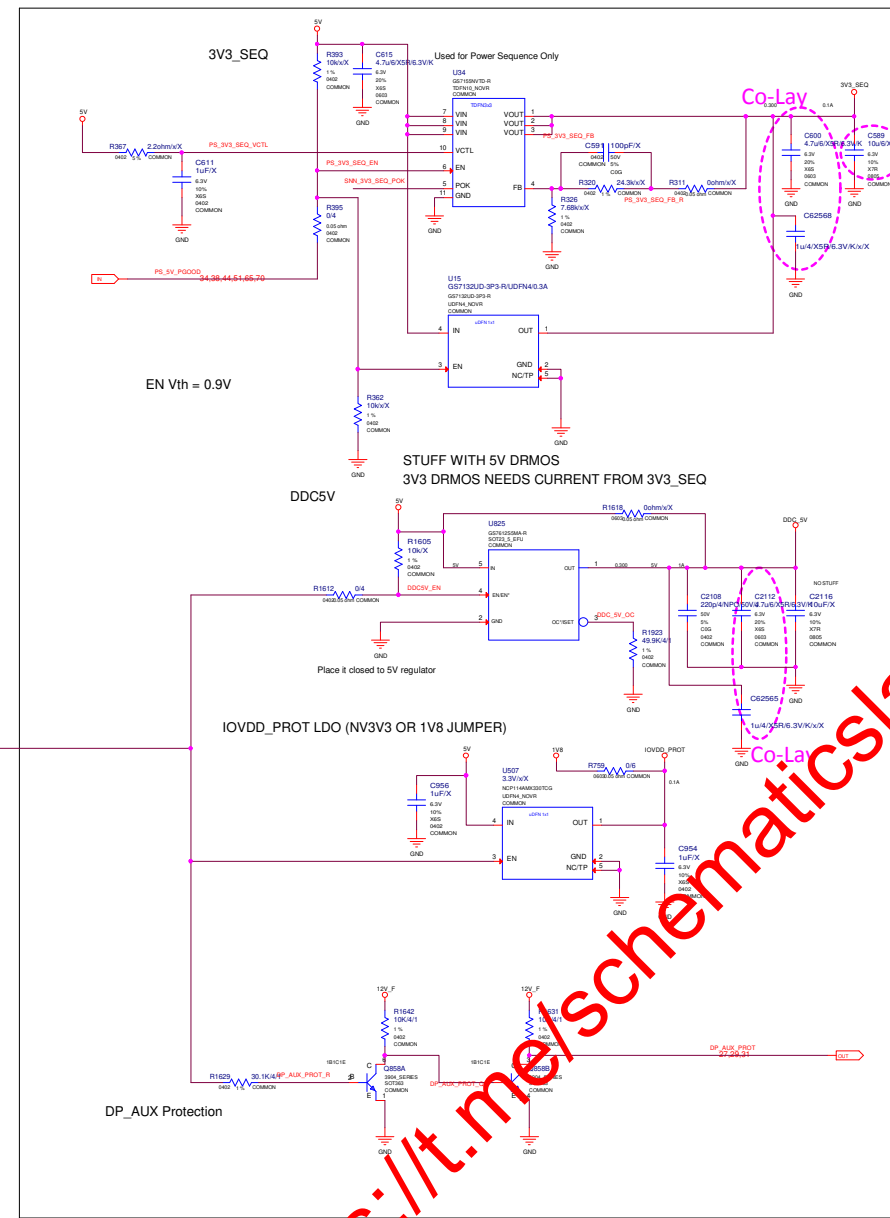
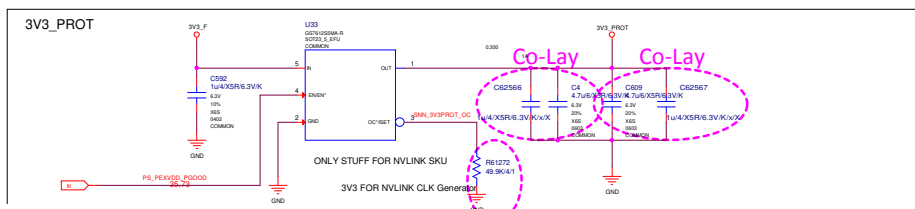
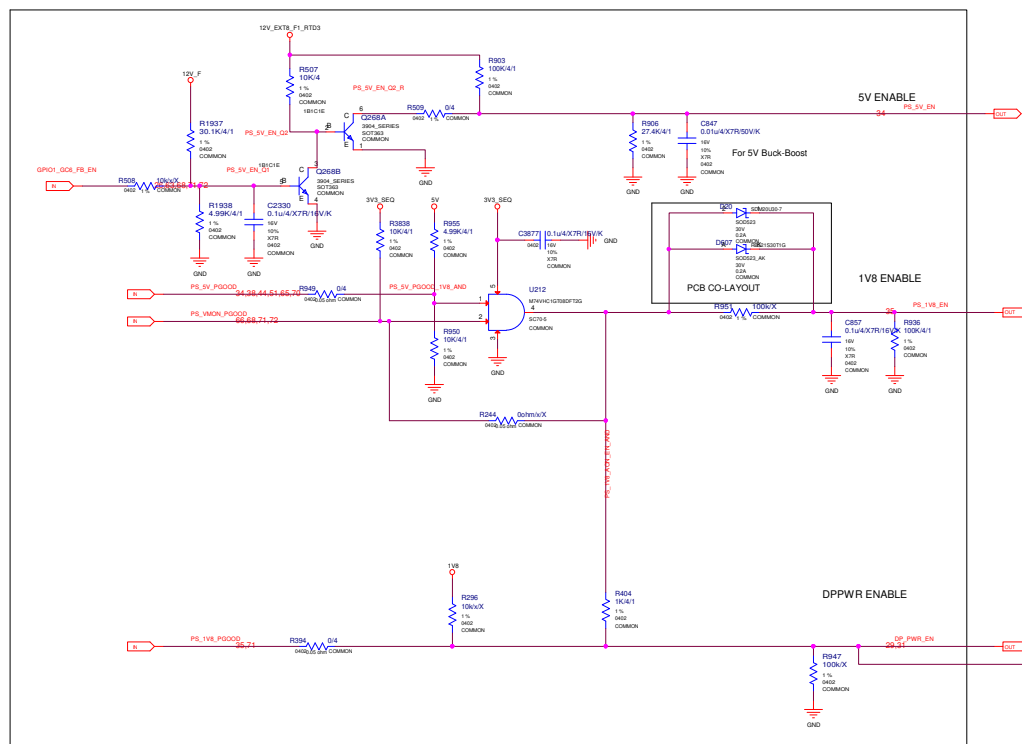
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Title			
PCIE			
Size	Document Number		Rev
Custom	GV-N3090EAGLE-24GD		1.0
Date:	Wednesday, September 30, 2020	Sheet	67 of 82

PS: Pre-Filter B

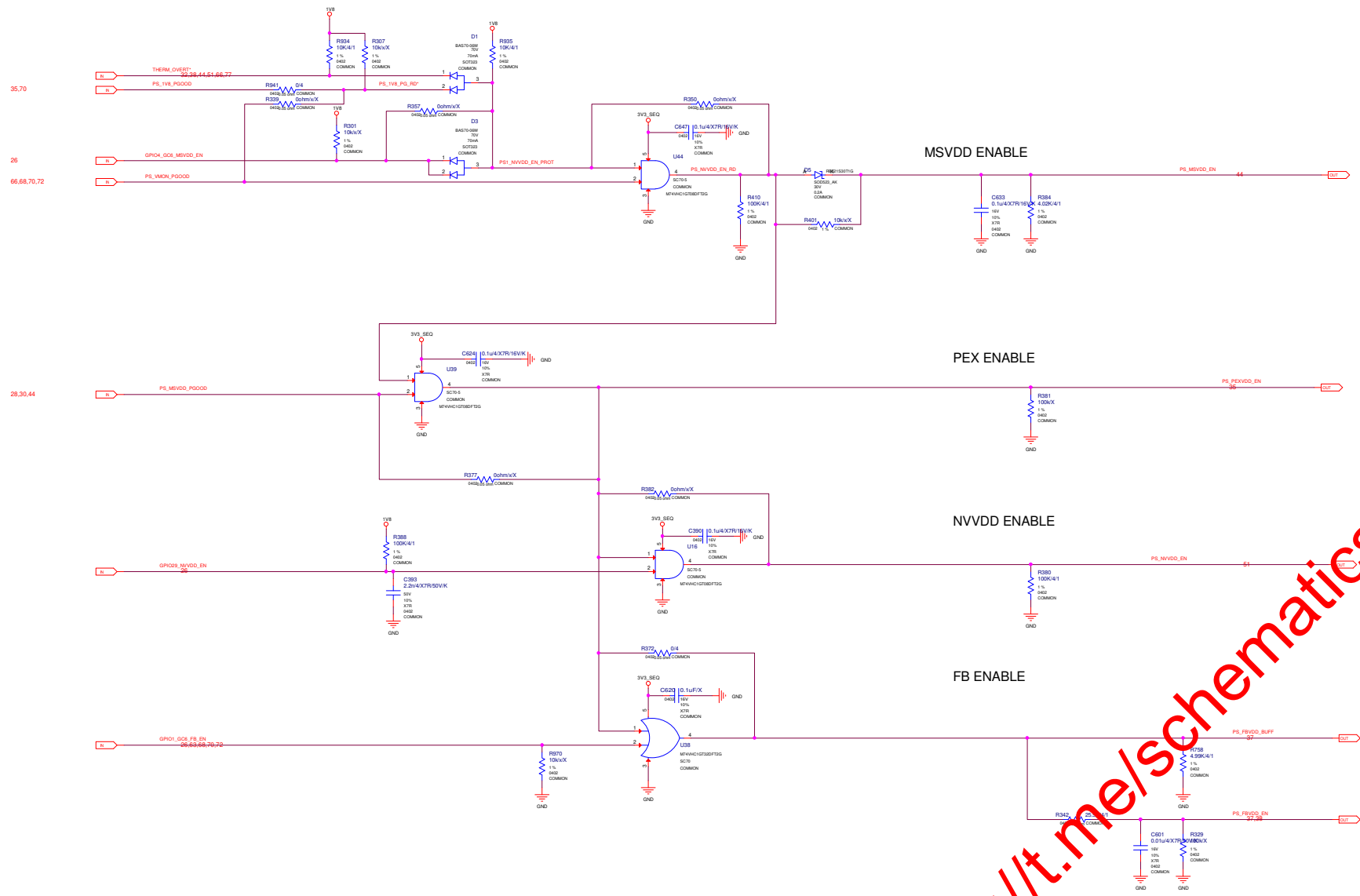


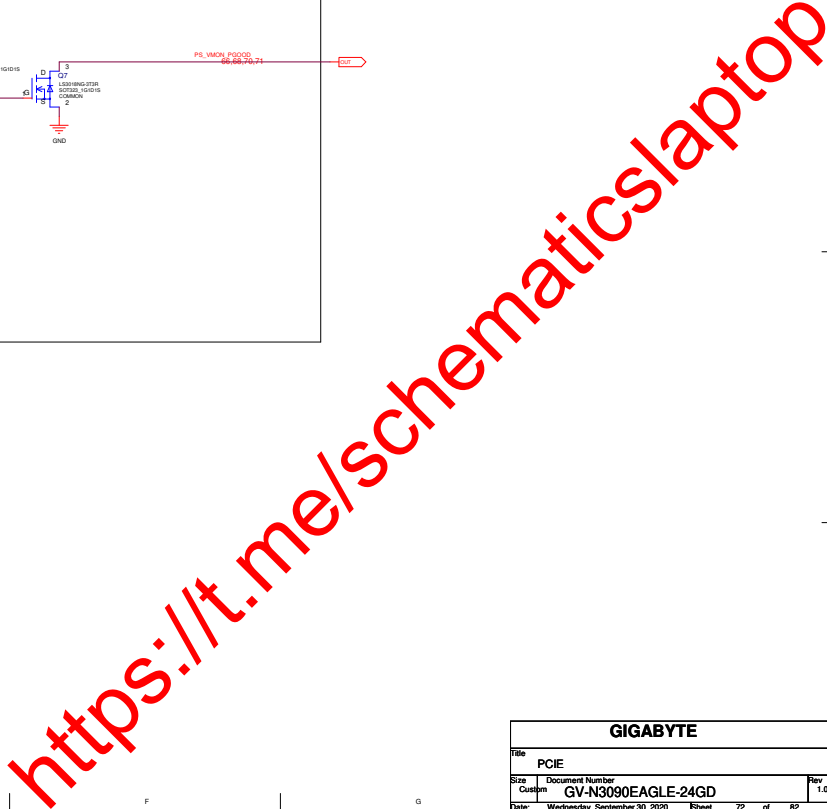
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SEQUENCE:5V,1V8,3V3_SEQ ENABLE

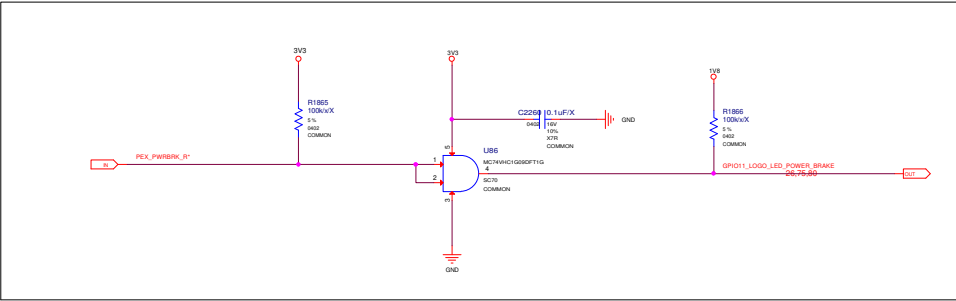
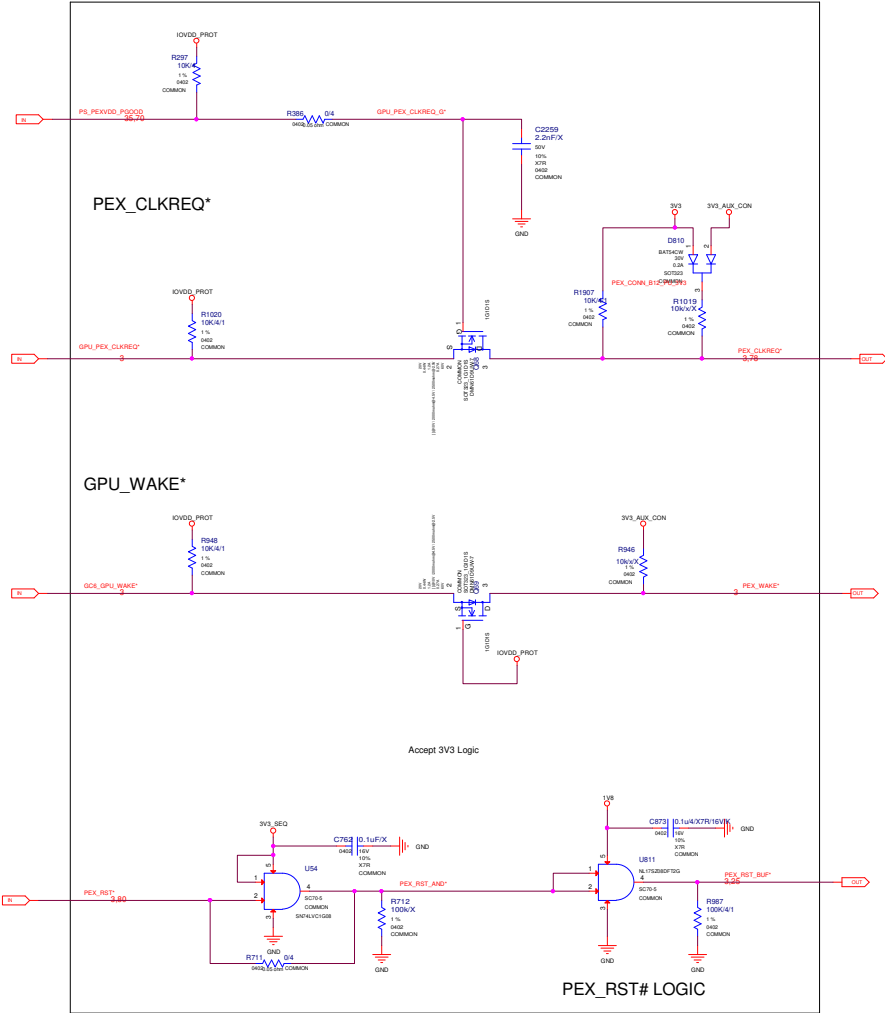


SEQUENCE:NV,PEX,FB ENABLE



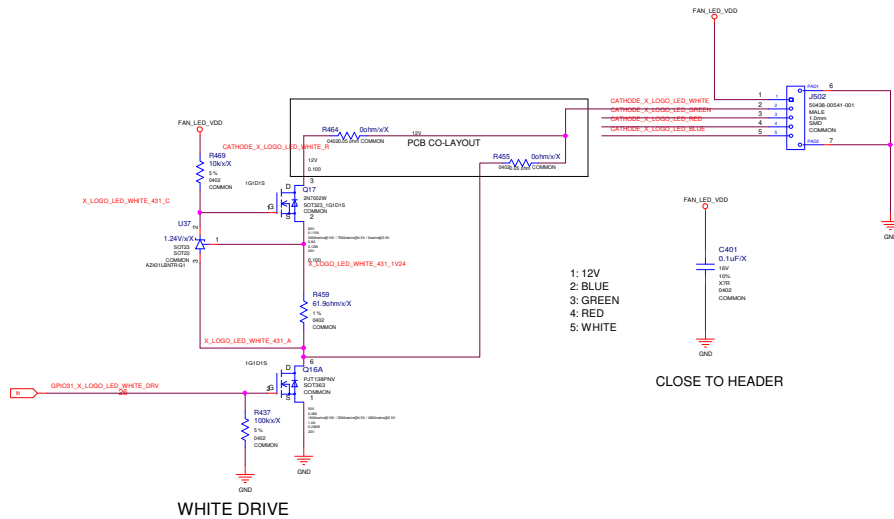


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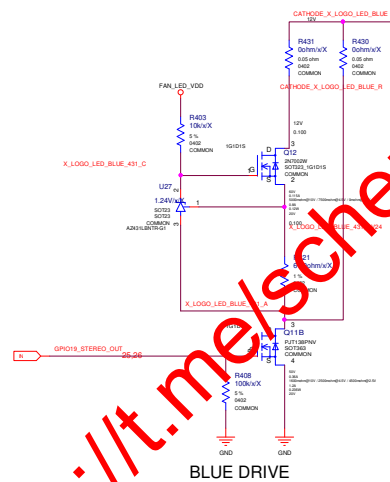
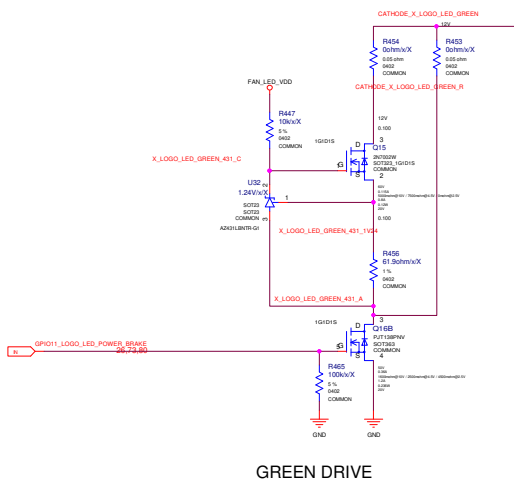
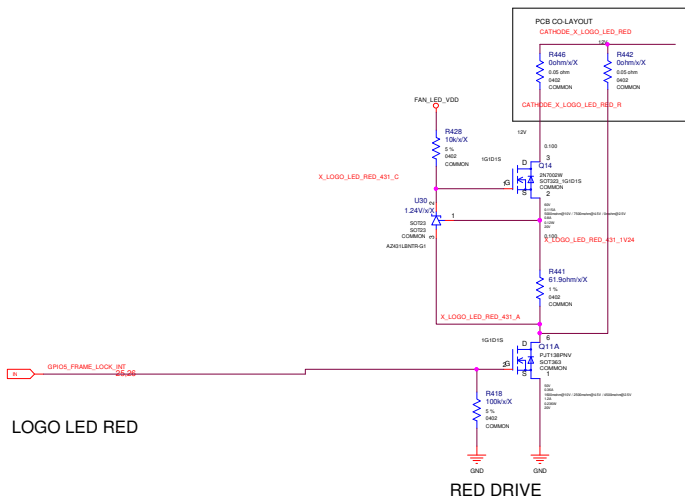


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CONNECTOR SIZE TO FIT LAYOUT
ALL CIRCUIT NO STUFF BY DEFAULT
ALINGN WITH PASH
IF ADDITIONAL POWER CONSUPTION R
NEEDED, IT SHOULD BE ON LED MOD.



Mechanical: Mounting holes

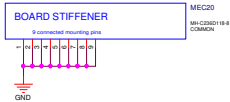
BK up



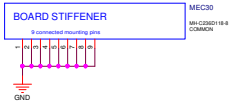
GPU



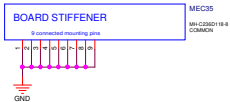
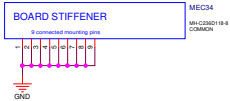
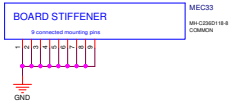
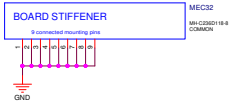
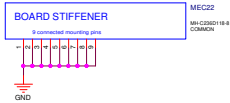
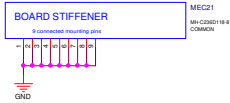
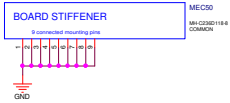
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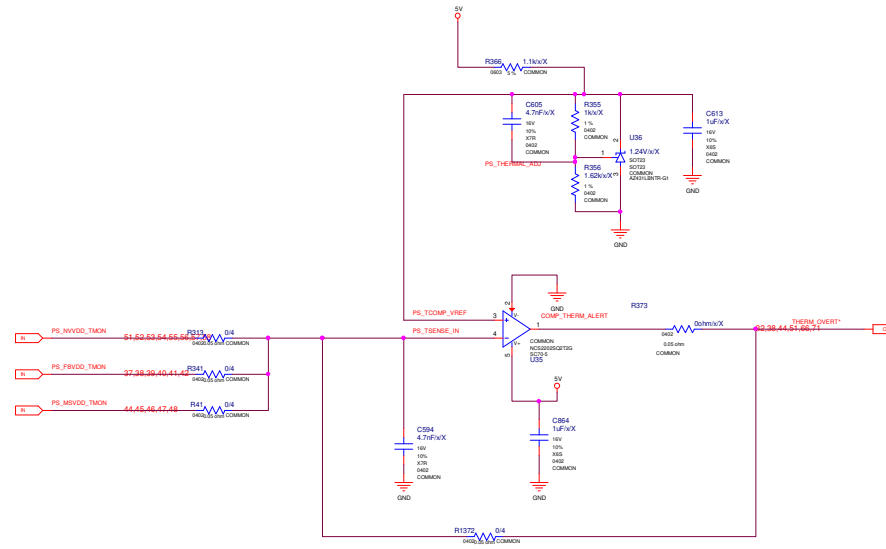
Backplane



BK low



VR THERMAL PROTECTION



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GIGABYTE				
Title				
PCIE				
Size	Design	Document Number	Revision	
		GV-N3090EAGLE-24GD		

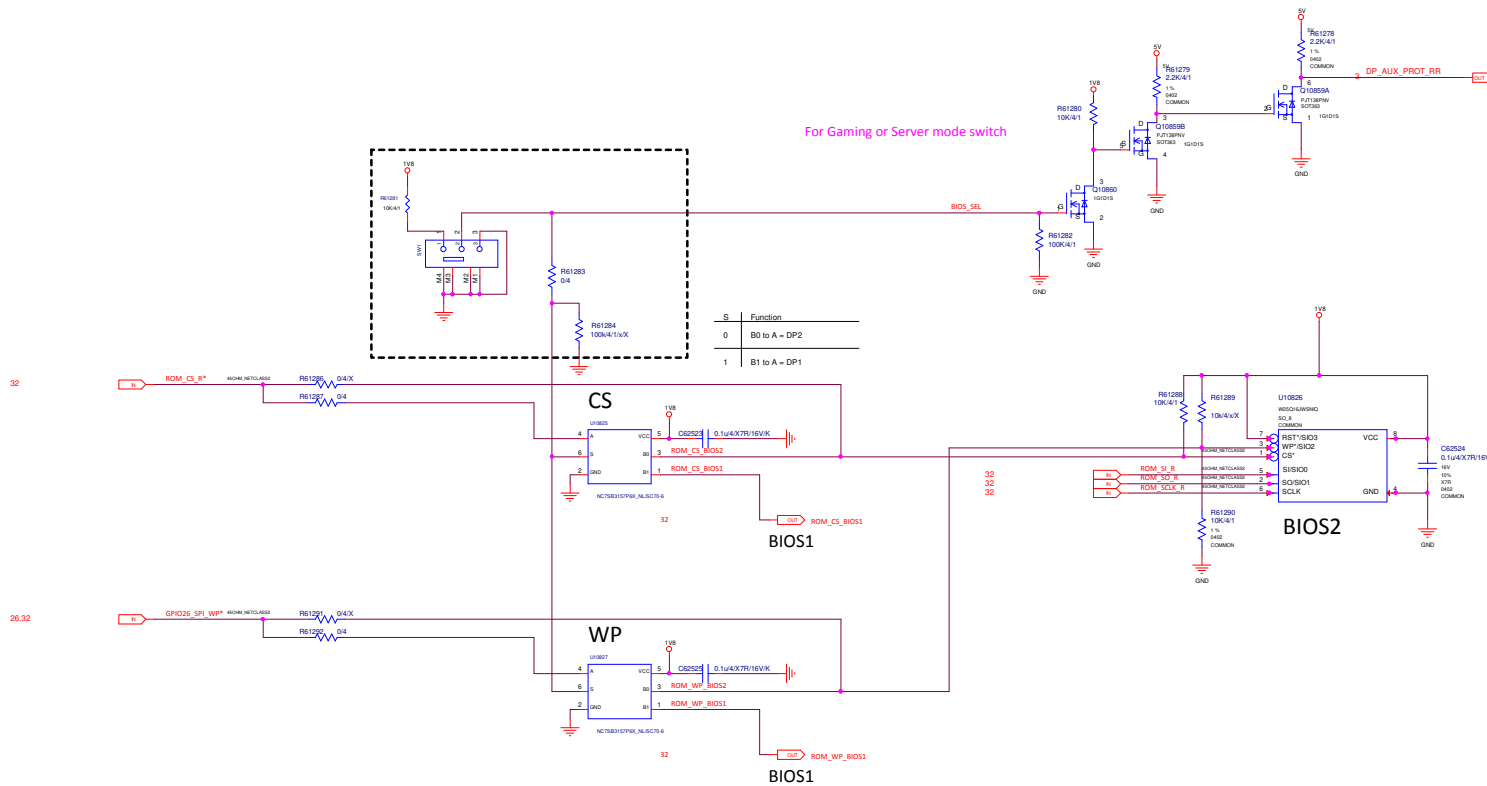
PCI TERM



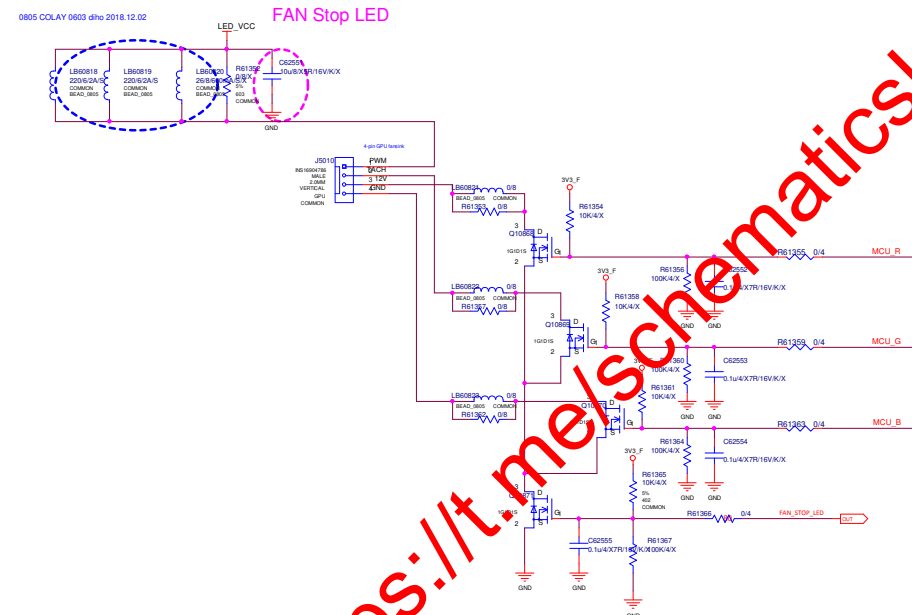
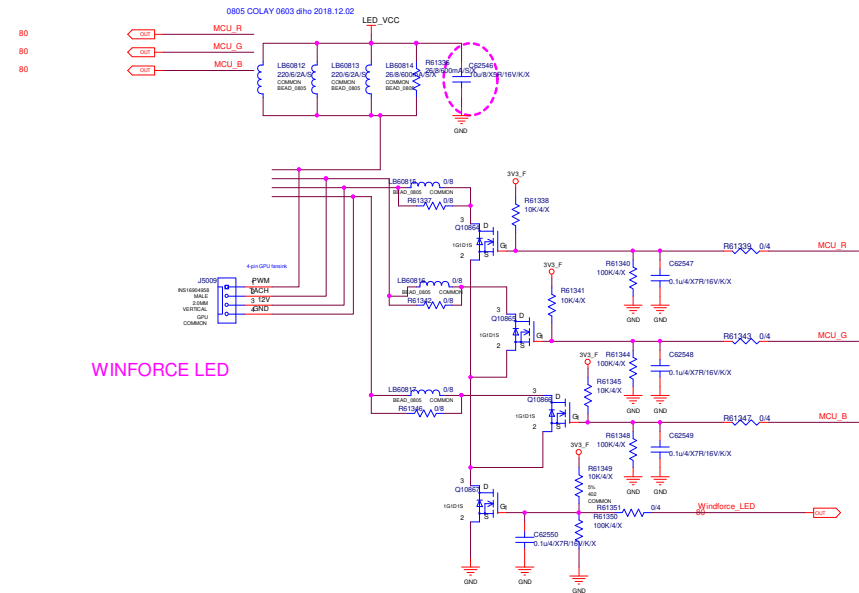
www.teknisi-indonesia.com

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BIOS Switch



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History

Date	Modify
2018.07.21	First version

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